

Department of Computer Science and Engineering
COURSE PLAN

SEMESTER	: IV (A,B,C)	NAME OF THE FACULTY	: USHA A.R
BRANCH	: CSE	DATE OF COMMENCEMENT	: 18 TH JAN 2016
SUBJECT	: ENNG. MATHS-4	DATE OF CLOSING	: 21 ST MAY 2016
SUBJECT CODE	: 10MAT41	CLASS STRENGTH	: 66
NO OF HRS/WK	: 6	TOTAL HRS	: 75

Session No.	Chapter No. (No of hours planned for the chapter)	Date	Topics planned for the Session	Teaching Aids	Assignments (IA) /Tests planned for the chapter	Topics covered As per plan
1	1/1	18/1/16	Unit I: Numerical Solution of ordinary differential equations of first order	Board, chalk, duster	Assignment- I	
2	2/1	19/1/16	Numerical methods for initial value problems	„		
3	3/1	20/1/16	Picard’s method	„		
4	4/1	23/1/16	Taylor’s series method	„		
5	5/1	23/1/16	Modified Euler’s method	„		
6	6/1	25/1/16	Runge-Kutta method of fourth order	„		
7	7/1	27/1/16	Predictor and corrector methods Milne’s Method	„		
8	8/1	28/1/16	Predictor and corrector methods Adams-Bashforth)	Board, chalk, duster		
9	9/1	29/1/16	Numerical Solution of ordinary differential equations of first order	„		
10	10/1	29/1/16	Numerical methods for initial value problems	„		
11	1/2	30/1/16	Unit II: Numerical solution of simultaneous first order ODEs	„	Assignment - II	
12	2/2	1/02/16	Picard’s Method	„		
13	3/2	2/02/16	Picard’s Method	„		

14	4/2	3/02/16	Problems on Picard's Method and Runge-Kutta method of fourth order	„		
15	5/2	4/02/16	Runge-Kutta method of fourth order			
16	6/2	5/02/16	Numerical solution of second order ODES - Picard's Method			
17	7/2	8/02/16	Problems on Picards method			
18	8/2	10/02/16	Numerical solution of second order ODES Runge-Kutta method	„		
19	9/2	11/02/16	Problems on Runge-Kutta method. concept on Milne's method	„		
20	10/2	12/02/16	Tutorial class	„		
21	1/3	13/02/16	Unit III: Introduction to Probability, Definitions	„	Assignment – III	
22	2/3	15/02/16	Probability theorems, addition theorem of probability	„		
23	3/3	17/02/16	problems			
24	4/3	18/02/16	Probability associated with set theory	Board, chalk, duster		
25	5/3	22/02/16	Random experiments, Sample Space and events	„		
26	6/3	23/02/16	Axioms of probability	„		
27	7/3	24/02/16	Conditional Probability, problems	„		
28	8/3	26/02/16	Multiplication Law, problems	„		
29	9/3	29/02/16	Baye's Theorem-proof	„		
30	10/3	1/03/16	Problems on Baye's Theorem	„		
31	1/4	2/03/16	Unit IV: Random Variables(Discrete random and continuous variables)	„	Assignmnt – IV	
32	2/4	3/03/16	Bernoulli's theorem,-Binomial Distribution(Mean and Standard deviation of the Binomial Distribution)	„		
33	3/4	5/03/16	Problems on Binomial Distribution	Board, chalk, duster		
34	4/4	8/03/16	Problems on Binomial Distribution	„		
35	5/4	9/03/16	Poisson distribution(Mean and Standard deviation of the Poisson Distribution)	„		

36	6/4	10/03/16	Continuous Probability distributions	„		
37	7/4	11/03/16	Exponential distribution(Mean and Standard deviation of the Exponential Distribution and problems)	„		
38	8/4	18/03/16	Normal distribution and Standard Normal distribution	„		
39	9/4	19/03/16	Problems on Normal distribution and Standard Normal distribution	„		
40	10/4	22/03/16	Problems on Normal distribution and Standard Normal distribution	„		
41	1/5	23/03/16	Unit V: Function of a complex variable, limit, continuity, differentiability	„	Assign ment - V	
42	2/5	28/03/16	Cauchy-Riemann equations in Cartesian and Polar form	„		
43	3/5	29/03/16	Harmonic function, orthogonal property	„		
44	4/5	30/03/16	Finding the derivative of an analytic function Milne-Thompson method	„		
45	5/5	31/03/16	Problems	„		
46	6/5	1/04/16	Finding the conjugate harmonic function and the analytic function	„		
47	7/5	4/04/16	Properties of analytic functions	„		
48	8/5	5/04/16	Harmonic Property	„		
49	9/5	6/04/16	Orthogonal Property	Board, chalk, duster		
50	10/5	7/04/16	Application to flow problems	„		
51	1/6	11/04/16	Unit VI: Conformal transformation	„	Assign ment - VI	
52	2/6	13/04/16	Bilinear transformation	„		
53	3/6	15/04/16	Discussion of $w = z^2$	„		
54	4/6	16/04/16	Discussion of $w = e^z$	„		
55	5/6	18/04/16	Problems	„		
56	6/6	20/04/16	Discussion of $w = z+a^2/z$	„		
57	7/6	22/04/16	Complex line integral	„		
58	8/6	23/04/16	Cauchy's theorem and integral formula	„		

59	1/7	28/04/16	Unit VII: Solution of Laplace Equation in cylindrical system leading to Bessel differential equation	„	Assignment - VII	
60	2/7	28/04/16	Solution of Laplace Equation in Spherical system leading to Bessel differential equation	„		
62	3/7	29/04/16	Properties on Bessel functions, Legendre's equation	„		
63	4/7	29/04/16	Bessel's function and properties	„		
64	5/7	30/04/16	Orthogonal Property of Bessel's function	„		
65	6/7	30/04/16	Series Solution of Legendre's Differential equation	„		
66	7/7	02/05/16	Rodrigue's formula-Derivation and problems	„		
67	1/8	03/05/16	Unit VIII: Sampling distribution	„	Assignment - VIII	
68	2/8	03/05/16	Testing Hypothesis	„		
69	3/8	04/05/16	Standard error	„		
70	4/8	04/05/16	Test for hypothesis for means	„		
71	5/8	05/05/16	Limits for means	„		
72	6/8	06/05/16	Student's <i>t</i> distribution	„		
73	7/8	07/05/16	Test of Significance of Difference between sample means	„		
74	8/8	10/05/16	Chi square distribution	„		
75	9/8	11/05/16	Sampling distribution	„		

Syllabus for Internal Assessment Tests (IAT)*

Sessional	Syllabus
T1	01-40
T2	41-66
T3	67-75

* See calendar of events for the schedules of IATs.

Literature:

Book Type	Code	Author & Title	Publication information	
			Edition & Publisher	ISBN
Text Book	TB1	B.S.Grewal, Higher Engineering Mathematics, Latest Edition, Khanna publishers	Latest edition, khanna publications	8174091955

Text Book	TB2	Erwin Kreyszig, Advanced Engineering Mathematics, Latest Edition, Wiley Publication	Wiley India publishers	978812653135
References	RB1	B.V.Ramana, Higher Engineering Mathematics,.	Latest Edition, Tata Mc.Graw Hill Publications	---
References	RB2	Peter V .O'Neil, Engineering Mathematics	Cengage Learning India Pvt. Ltd.Publishers	---
References	RB3	Dr.D.S.C, Engineering Mathematics IV	5 th Edition 2011	978-81-7686-675-4
References	RB4	Dr.K.S.C, Engineering Mathematics IV	2011-2012	---

SEMESTER : IV
BRANCH : CSE
SUBJECT : Unix and Shell Programming
SUBJECT CODE : 10CS44
NO OF HRS/WK : 5

NAME OF THE FACULTY : GOURISH
DATE OF COMMENCEMENT : 27.01.2016
DATE OF CLOSING : 21.05.2016
CLASS STRENGTH : 66
TOTAL HRS : 73

Session No	Chapter no (No of hrs planed for the chapter) (UNIT#/HRS)	DATE	Topics planned for the session	Teaching Aids	Assignments/ Tests planned for the chapter	Topics covered As per plan
1	1/1	18.01.16	Introduction	Chalk & Talk		
2	1/2	19.01.16	Basics of OS	„		
3	1/3	20.01.16	Boot Sequence of OS	„		
4	1/4	21.01.16	Unix Architecture and system calls	„		
5	1/5	22.01.16	Features of Unix	„		
6	1/6	23.01.16	Locating commands,structure and man	„		
7	1/7	25.01.16	Help,apropos and control keys	“		
8	1/8	27.01.16	Filename,file systems and parent-child relationship	„		
9	1/9	28.01.16	Absolute path and relative paths,cd ,mkdir,rmdir,pwd	„		
10	1/10	29.01.16	Ls and it options	„		
11	1/11	30.01.16	Live Session/Revision	Laptops		
12	1/12	01.02.16	Class Test on Unit I	„	Test	
13	2/1	02.02.16	Ls -l,ls -ld,file ownership,chmod	„		
14	2/2	03.02.16	Relative and absolute permissions	„		
15	2/3	04.02.16	Security implications,-R,Dir perm,chown and chgrp	“		
16	2/4	05.02.16	The Vi modes,Input mode	„		

17	2/5	08.02.16	Save and quit	”		
18	2/6	09.02.16	Editing text	”		
19	2/7	10.02.16	Search and replace	”		
20	2/8	11.02.16	Live Session/ Revision	Laptops	Assignment I	
21	3/1	12.02.16	Shell : Pattern matching,escape,quoting	”		
22	3/2	13.02.16	Redirection,special files and pipes	”		
23	3/3	15.02.16	Tee,command substitution and shell variables	”		
24	3/4	16.02.16	Process basics, ps,e,A,fork,exec wait.	”		
25	3/5	17.02.16	Background jobs and killing jobs	”		
26	3/6	18.02.16	Job Control	”		
27	3/7	22.02.16	Env Variables,HOME,LOGNAME and aliases.	”		
28	3/8	23.02.16	Command History and Inline command editing	”		
29	4/1	24.02.16	Live Session/Revision	Laptops		
30	4/2	25.02.16	File Attr: Hard and softlinks	”		
31	4/3	26.02.16	Directory and umask,Modification time	”		
32	4/4	29.02.16	Access time ,touch and find	”		
33	4/5	01.03.16	Find criteria, pr,head ,tail	”		
34	4/6	02.03.16	Cut,paste	”		
35	4/7	03.03.16	sort,uniq,tr	”		
36	4/8	04.03.16	Class Test 2 on Unit 3,4	”	Test	
37	5/1	05.03.16	Grep,grep	”		
38	5/2	08.03.16	*,?,[]	”		
39	5/3	09.03.16	Sed:line addressing, Multiple instructions	”		
40	5/4	10.03.16	Context addressing,	”		
41	5/5	11.03.16	Editing,	”		
42	5/6	17.03.16	writing lines to file,	”		
43	5/7	18.03.16	Deleting lines,	”		
44	5/8	19.03.16	substitution	”		
45	5/9	21.03.16	Substitution contd, Repeat pattern,	”		
46	5/10	22.03.16	Interval RE, and tagged RE.	”		

47	5/11	23.03.16	Live Session	Laptops	Assignment II	
48	6/1	24.03.16	Shell Scripts,read	„		
49	6/2	28.03.16	Using cmd args,exit status	„		
50	6/3	29.03.16	The logical ,if ,Numeric and string comparison	„		
51	6/4	30.03.16	Case ,file tests,	„		
52	6/5	31.03.16	Expr,positional parameters	„		
53	6/6	01.04.16	loops(for ,while)	„		
54	6/7	02.04.16	Set and shift	„		
55	6/8	04.04.16	Here document,trap	„		
56	6/9	05.04.16	Sample program 1	„		
57	6/10	06.04.16	Sample program 2	„		
58	6/11	07.04.16	Sample program 3	„		
59	6/12	11.04.16	Class Test III on Unit 5	Test		
60	7/1	12.04.16	Awk scripting,filtering,split,	„		
61	7/2	13.04.16	var and exp,comparisons	„		
62	7/3	15.04.16	Built in Vars	„		
63	7/4	16.04.16	Arrays	„		
64	7/5	18.04.16	Functions and control flow	„		
65	7/6	20.04.16	loops	„		
66	7/7	21.04.16	Programming example 1	„		
67	7/8	22.04.16	Programming example 1	„		
67	7/9	23.04.16	Programming example 1	„	Assignment III	
68	8/1	28.04.16	Perl,chop	„		
69	8/2	29.04.16	Vars and exp,functions	„		
70	8/3	30.04.16	Cmd args,lists	„		
71	8/4	02.05.16	Arrays,foreach,split	„		
72	8/5	03.05.16	File tests	„		
73	8/6	04.05.16	Regular expressions	„		
74	8/7	05.05.16	Programming Examples1	„		
75	8/8	06.05.16	Programming Examples2	„		
76	8/1	07.05.16	REVISION	„		

Syllabus for Internal Assessment Tests (IAT) *

IAT #	Syllabus
IAT-1	Class # 01 – 35
IAT-2	Class # 36 - 67
IAT-3	Class # 68- 76

Literature:

Book Type	Code	Author & Title	1. Publication information	
			Edition // Publisher	ISBN #
Text Book	TB1	Sumitabha Das, “ UNIX – Concepts and Applications”	Fourth Edition, Tata McGraw Hill	0-07-063546-3
Reference	RB1	Behrouz A Forouzan and Richard F Gilberg, “ UNIX and Shell Programming “	Cengage Learning, INDIA Edition, Ninth Indian Reprint 2009	978-81-315-0325-6
Reference	RB2	M.G. Venkateshmurthy, “ Introduction to UNIX and Shell Programming “	Pearson Education, 2005, Eighth Impression 2011	978-81-7758-745-6

SEMESTER : IV A
 BRANCH : CSE
 SUBJECT : MICROPROCESSORS
 SUBJECT CODE : 10CS45
 NO OF HRS/WK : 5

NAME OF THE FACULTY : SUDHAKAR K. N
 DATE OF COMMENCEMENT : 18TH JAN 2016
 DATE OF CLOSING : 21ST MAY 2016
 CLASS STRENGTH : 66
 TOTAL HRS : 54

Session No.	Chapter No. (No of hours planned for the chapter)	Date	Topics planned for the Session	Teaching Aids	Assignments (IA) /Tests planned for the chapter	Topics covered As per plan
1	1/1	19/01/2016	Unit I: Prerequisites and importance of learning concepts over microprocessors.	Chalk & Talk		
2	2/1	20/01/2016	A Historical Background of microprocessors and its evolution.	„		
3	3/1	20/01/2016	The Microprocessor-Based Personal Computer Systems	”	IA- I	
4	4/1	22/01/2016	Internal Microprocessor Architecture	”		
5	5/1	23/01/2016	Register Organization and Flag register structure.	”		
6	6/1	27/01/2016	Flag register and its influence on programming.	”		
7	7/1	28/01/2016	Real Mode Memory Addressing	”		
8	1/2	28/01/2016	Unit II: Protected Mode Memory Addressing	”		

9	2/2	30/01/2016	Memory Paging	”		
10	3/2	01/02/2016	Flat Mode Memory	”	IA -II	
11	4/2	03/02/2016	Data Addressing Modes	”		
12	5/2	04/02/2016	Data Addressing Modes continues	”		
13	6/2	04/02/2016	Program Memory Addressing Modes	”		
14	7/2	08/02/2016	Stack Memory Addressing Modes	”		
15	1/3	09/02/2016	Unit III: Data Movement Instructions: MOV Instruction Revisited	”		
16	2/3	11/02/2016	Stack operations PUSH/POP, LEA LES	”		
17	3/3	12/02/2016	Assembler Details	”	IA –III	
18	4/3	12/02/2016	Assembler Directives	”		
19	5/3	15/02/2016	Comparison and effect of flags over it.	”		
20	6/3	16/02/2016	String Instructions: String Data Transfers, Miscellaneous String Data Transfers	”		
21	7/3	18/02/2016	Miscellaneous Data Transfer Instructions, Segment Override Prefix	”		
22	8/3	22/02/2016	Arithmetic Instructions: Addition, Subtraction	”		
23	9/3	22/02/2016	Multiplication and Division.	”		
24	1/4	24/02/2016	Unit IV: BCD and ASCII Arithmetic.	”		
25	2/4	25/02/2016	Basic Logic Instructions, Shift and Rotate.	”		
26	3/4	29/02/2016	String instructions: String Comparisons and programming examples	”	IA–IV	
27	4/4	01/03/2016	Program Control Instructions: The Jump Group Controlling the Flow of the program.	”		
28	5/4	01/03/2016	Procedures and Macros with examples.	”		
29	6/4	03/03/2016	Introduction to Interrupts (INT 21H and INT 10H)	”		
30	7/4	04/03/2016	Machine Control and Miscellaneous Instructions.	”		
31	1/8	08/03/2016	UNIT VIII: I/O Interface basics and IN and OUT instruction revisited	”		
32	2/8	09/03/2016	Block diagram of Programmable Peripheral Interface 82C55	”	IA-V	
33	3/8	09/03/2016	82C55 organization of ports and how we can access them.	”		
34	4/8	11/03/2016	Programmable Interval Timer 8254	”		
35	5/8	17/03/2016	Interrupts: Basic Interrupt Processing. Details about IVT	”		

36	6/8	19/03/2016	Hardware Interrupts: INTR and INTA	”		
37	7/8	21/03/2016	Direct Memory Access: Basic DMA Operation and Definition.	”		
38	8/8	21/03/2016	The 8237 DMA Controller.	”		
39	1/5	23/03/2016	Unit V: Pin-Outs and the Pin Functions of 8086	”		
40	2/5	24/03/2016	Min and Max mode of 8086 and 8088	”	IA -VI	
41	3/5	29/03/2016	Clock Generator	”		
42	4/5	30/03/2016	Bus Buffering and Latching	”		
43	5/5	30/03/2016	Bus Timings Ready and Wait state	”		
44	6/5	01/04/2016	Minimum Mode versus Maximum Mode	”		
45	7/5	02/04/2016	Memory Interfacing: Memory Devices	”		
46	8/5	05/04/2016	Interfacing continues with comparison with 8086 & 8086.	”		
47	1/6	06/04/2016	UNIT VI: Introduction to combine Assembly Language with C/C++: Using Assembly Language with C/C++ for 16-Bit DOS Applications	”		
48	2/6	06/04/2016	Using Assembly Language with C/C++ for 16-Bit DOS Applications Continues	”	IA -VII	
49	3/6	11/04/2016	Using Assembly Language with C/C++ for 16-Bit DOS Applications Continues	”		
50	4/6	12/04/2016	Using Assembly Language with C/C++ for 32-Bit DOS Applications	”		
51	5/6	15/04/2016	Using Assembly Language with C/C++ for 32-Bit DOS Applications Continues	”		
52	6/6	16/04/2016	Modular Programming	”		
53	7/6	16/04/2016	Modular Programming Continues	”		
54	8/6	20/04/2016	Programming using the Keyboard and Video Display	”		
55	9/6	21/04/2016	Programming using the Keyboard and Video Display continues	”		
56	1/7	23/04/2016	UNIT VII: Memory Interfacing (continued): Address Decoding	”		
57	2/7	28/04/2016	8086 Memory Interface.	”	IA -VIII	
58	3/7	28/04/2016	8086 Memory Interface continues	”		

59	4/7	30/04/2016	Implementation using ROM	”		
60	5/7	02/05/2016	Implementation using ROM continues	”		
61	6/7	04/05/2016	Implementation using ROM continues	”		
62	7/7	05/05/2016	Basic I/O Interface	”		
63	8/7	05/05/2016	I/O Interface continues	”		
64	9/7	07/05/2016	I/O Port Address Decoding	”		
65	10/7	10/05/2016	I/O Port Address Decoding Continues.	”		

Syllabus for Internal Assessment Tests (IAT)*

Sessional #	Syllabus
IAT1	Class # 01 – 30
IAT2	Class # 31 – 55
IMP_IAT	Class # 56 – 65

*: See calendar of events for the schedules of IATs.

Literature:

Book Type	Code	Author & Title	Publication info	
			Edition & Publisher	ISBN #
Text Book	TB1	Barry B Brey: The Intel Microprocessors, 8th Edition, Pearson Education, 2009. (Listed topics only from the Chapters 1 to 13)	8 th Edition, Tata McGraw Hill,2009	9780135026458
References	RB1	Douglas V. Hall: Microprocessors and Interfacing, Revised 2nd Edition, TMH, 2006.	2 nd Edition, Tata McGraw Hill,2006	978-0-07-060167-3
References	RB2	K. Udaya Kumar & B.S. Umashankar Advanced Microprocessors & IBM-PC Assembly Language Programming, TMH 2003.	Tata McGraw Hill,2011	978-0-07-463430-1

SEMESTER : IV B
BRANCH : CSE
SUBJECT : MICROPROCESSORS
SUBJECT CODE : 10CS45
NO OF HRS/WK : 5

NAME OF THE FACULTY : SUDHAKAR K. N
DATE OF COMMENCEMENT : 18TH JAN 2016
DATE OF CLOSING : 21ST MAY 2016
CLASS STRENGTH : 66
TOTAL HRS : 54

Session No.	Chapter No. (No of hours planned for the chapter)	Date	Topics planned for the Session	Teaching Aids	Assignments (IA) /Tests planned for the chapter	Topics covered As per plan
1	1/1	18/01/2016	Unit I: Prerequisites and importance of learning concepts over microprocessors.	Chalk & Talk		
2	2/1	18/01/2016	A Historical Background of microprocessors and its evolution.	„		
3	3/1	19/01/2016	The Microprocessor-Based Personal Computer Systems	„	IA- I	
4	4/1	21/01/2016	Internal Microprocessor Architecture	„		
5	5/1	22/01/2016	Register Organization and Flag register structure.	„		
6	6/1	25/01/2016	Flag register and its influence on programming.	„		
7	7/1	25/01/2016	Real Mode Memory Addressing	„		
8	1/2	27/01/2016	Unit II: Protected Mode Memory Addressing	„		
9	2/2	29/01/2016	Memory Paging	„		
10	3/2	30/01/2016	Flat Mode Memory	„	IA -II	
11	4/2	02/02/2016	Data Addressing Modes	„		
12	5/2	02/02/2016	Data Addressing Modes continues	„		
13	6/2	03/02/2016	Program Memory Addressing Modes	„		
14	7/2	05/02/2016	Stack Memory Addressing Modes	„		
15	1/3	08/02/2016	Unit III: Data Movement Instructions: MOV Instruction Revisited	„		
16	2/3	10/02/2016	Stack operations PUSH/POP, LEA LES	„		
17	3/3	10/02/2016	Assembler Details	„	IA –III	
18	4/3	11/02/2016	Assembler Directives	„		
19	5/3	13/02/2016	Comparison and effect of flags over it.	„		
20	6/3	15/02/2016	String Instructions: String Data Transfers, Miscellaneous String Data Transfers	„		
21	7/3	17/02/2016	Miscellaneous Data Transfer Instructions, Segment Override Prefix	„		
22	8/3	17/02/2016	Arithmetic Instructions: Addition, Subtraction	„		

23	9/3	18/02/2016	Multiplication and Division.	”		
24	1/4	23/02/2016	Unit IV: BCD and ASCII Arithmetic.	”		
25	2/4	24/02/2016	Basic Logic Instructions, Shift and Rotate.	”		
26	3/4	26/02/2016	String instructions: String Comparisons and programming examples	”	IA-IV	
27	4/4	26/02/2016	Program Control Instructions: The Jump Group Controlling the Flow of the program.	”		
28	5/4	29/02/2016	Procedures and Macros with examples.	”		
29	6/4	02/03/2016	Introduction to Interrupts (INT 21H and INT 10H)	”		
30	7/4	03/03/2016	Machine Control and Miscellaneous Instructions.	”		
31	1/8	05/03/2016	UNIT VIII: I/O Interface basics and IN and OUT instruction revisited	”		
32	2/8	05/03/2016	Block diagram of Programmable Peripheral Interface 82C55	”	IA-V	
33	3/8	08/03/2016	82C55 organization of ports and how we can access them.	”		
34	4/8	10/03/2016	Programmable Interval Timer 8254	”		
35	5/8	11/03/2016	Interrupts: Basic Interrupt Processing. Details about IVT	”		
36	6/8	18/03/2016	Hardware Interrupts: INTR and INTA	”		
37	7/8	18/03/2016	Direct Memory Access: Basic DMA Operation and Definition.	”		
38	8/8	19/03/2016	The 8237 DMA Controller.	”		
39	1/5	22/03/2016	Unit V: Pin-Outs and the Pin Functions of 8086	”		
40	2/5	23/03/2016	Min and Max mode of 8086 and 8088	”	IA -VI	
41	3/5	28/03/2016	Clock Generator	”		
42	4/5	28/03/2016	Bus Buffering and Latching	”		
43	5/5	29/03/2016	Bus Timings Ready and Wait state	”		
44	6/5	31/03/2016	Minimum Mode versus Maximum Mode	”		
45	7/5	01/04/2016	Memory Interfacing: Memory Devices	”		
46	8/5	04/04/2016	Interfacing continues with comparison with 8086 & 8086.	”		

47	1/6	04/04/2016	UNIT VI: Introduction to combine Assembly Language with C/C++: Using Assembly Language with C/C++ for 16-Bit DOS Applications	”		
48	2/6	05/04/2016	Using Assembly Language with C/C++ for 16-Bit DOS Applications Continues	”	IA -VII	
49	3/6	07/04/2016	Using Assembly Language with C/C++ for 16-Bit DOS Applications Continues	”		
50	4/6	11/04/2016	Using Assembly Language with C/C++ for 32-Bit DOS Applications	”		
51	5/6	13/04/2016	Using Assembly Language with C/C++ for 32-Bit DOS Applications Continues	”		
52	6/6	13/04/2016	Modular Programming	”		
53	7/6	15/04/2016	Modular Programming Continues	”		
54	8/6	18/04/2016	Programming using the Keyboard and Video Display	”		
55	9/6	20/04/2016	Programming using the Keyboard and Video Display continues	”		
56	1/7	22/04/2016	UNIT VII: Memory Interfacing (continued): Address Decoding	”		
57	2/7	22/04/2016	8086 Memory Interface.	”	IA -VIII	
58	3/7	23/04/2016	8086 Memory Interface continues	”		
59	4/7	29/04/2016	Implementation using ROM	”		
60	5/7	30/05/2016	Implementation using ROM continues	”		
61	6/7	03/05/2016	Implementation using ROM continues	”		
62	7/7	03/05/2016	Basic I/O Interface	”		
63	8/7	04/05/2016	I/O Interface continues			
64	9/7	06/05/2016	I/O Port Address Decoding	”		
65	10/7	07/05/2016	I/O Port Address Decoding Continues.	”		

Syllabus for Internal Assessment Tests (IAT)*

Sessional #	Syllabus
IAT1	Class # 01 – 30
IAT2	Class # 31 – 55
IMP_IAT	Class # 56 – 65

*: See calendar of events for the schedules of IATs.

Literature:

Book Type	Code	Author & Title	Publication info	
			Edition & Publisher	ISBN #
Text Book	TB1	Barry B Brey: The Intel Microprocessors, 8th Edition, Pearson Education, 2009. (Listed topics only from the Chapters 1 to 13)	8 th Edition, Tata McGraw Hill,2009	9780135026458
References	RB1	Douglas V. Hall: Microprocessors and Interfacing, Revised 2nd Edition, TMH, 2006.	2 nd Edition, Tata McGraw Hill,2006	978-0-07-060167-3
References	RB2	K. Udaya Kumar & B.S. Umashankar Advanced Microprocessors & IBM-PC Assembly Language Programming, TMH 2003.	Tata McGraw Hill,2011	978-0-07-463430-1

SEMESTER : IV -A
BRANCH : CSE
SUBJECT : Computer Organization
SUBJECT CODE : 10CS46
NO OF HRS/WK : 5

NAME OF THE FACULTY : Harikrishnan R S
DATE OF COMMENCEMENT : 18.01.2016
DATE OF CLOSING : 11.05.2016
CLASS STRENGTH : 60
TOTAL HRS : 65

Session No	Chapter no (No of hrs planed for the chapter)	DATE	Topics planned for the session	Teaching Aids	Assignments/ Tests planned for the chapter	Topics covered As per plan
1		18.01.16	Prerequisites	Chalk & Talk		
2		19.01.16	Prerequisites	”		
3		20.01.16	Prerequisites	”		
4	1/1	21.01.16	Computer Types, Historical Perspectives, Functional Units	”		
5	2/1	23.01.16	Basic operational concept, Performance Equation, Performance Measurement	”		
6	3/1	25.01.16	Numbers, Arithmetic Operations and Characters	”	Assignment-I	
7	4/1	26.01.16	Memory Location & Addresses	”		
8	5/1	27.01.16	Instructions and Instruction Sequencing	”		
9	6/1	28.01.16	Instructions and Instruction Sequencing	”		
10	7/1	30.01.16	Instructions and Instruction Sequencing	”		
11	1/2	2.02.16	Addressing Mode	”		

12	2/2	3.02.16	Assembly language	”		
13	3/2	4.02.16	Basic Input and Output Operations, Stacks and Queues	”		
14	4/2	5.02.16	Additional Instructions, Encoding of machine Instructions	”	Assignment - II	
15	5/2	.02.16	Subroutines	“		
16	6/2	8.02.16	Subroutines	”		
17	1/3	10.02.16	Accessing I/O Devices	”		
18	2/3	11.02.16	Interrupts-Interrupt Hardware, Enabling and Disabling Interrupts	”		
19	3/3	12.02.16	Interrupts-Handling Multiple Devices	”		
20	4/3	13.02.16	DMA	”	Assignment – III	
21	5/3	15.02.16	Buses	”		
22	6/3	17.02.16	Buses	”		
23	7/3	18.02.16	Exceptions	“		
24	1/4	22.02.16	Interface Circuits	”		
25	2/4	23.02.16	Interface Circuits	”		
26	3/4	24.02.16	Interface Circuits	”		
27	4/4	26.02.16	Interface Circuits	”		
28	5/4	29.02.16	PCI bus	”		
29	6/4	1.03.16	PCI bus, SCSI bus	”	Assignment – IV	
30	7/4	2.03.16	SCSI bus, USB	”		
31	8/4	3.03.16	USB	”		
32	9/4	5.03.16	USB	“		
33	1/6	8.03.16	Addition and Subtraction of Signed Numbers	”		
34	2/6	9.03.16	Design of fast adders	”		
35	3/6	10.03.16	Multiplication of positive numbers	”		
36	4/6	11.03.16	Signed operand multiplication,	”	Assignment - V	
37	5/6	18.03.16	Fast multiplication	”		

38	6/6	19.03.16	Integer Division	”		
39	7/6	21.03.16	Integer Division	“		
40	8/6	22.03.16	Floating point number and operations	”		
41	9/6	23.03.16	Floating point number and operations	”		
42	1/8	28.03.16	Performance, The Power wall	”		
43	2/8	29.03.16	The switch from Uni-processor to multiprocessor, Amdahl’s law,	”		
44	3/8	30.03.16	Shared memory, Multiprocessors, Clusters, Message passing multiprocessors	”		
45	4/8	31.03.16	Hardware multithreading	”		
46	5/8	1.04.16	SISD, MIMD, SIMD, SPMD, Vector	”		
47	6/8	4.04.16	Sums	“		
48	1/7	5.04.16	Some fundamental concept	”		
49	2/7	6.04.16	Execution of complete instruction	”		
50	3/7	7.04.16	Execution of complete instruction	”		
51	4/7	11.04.16	Multiple bus organization	”		
52	5/7	13.04.16	Hard wired control	”		
53	6/7	15.04.16	Micro-programmed control	”		
54	7/7	16.04.16	Micro-programmed control	”		
55	1/5	18.04.16	Basic Concepts	“		
56	2/5	20.04.16	Semiconductor RAM Memories	”		
57	3/5	22.04.16	Read only memory, Speed, Size and Cost.	”		
58	4/5	23.04.16	Cache Memories-Mapping Function	”		
59	5/5	28.04.16	Replacement algorithms, cache-sums	”		
60	6/5	29.04.16	Performance Considerations	”		
61	7/5	30.04.16	Performance Considerations			

62	8/5	3.05.16	Virtual Memories			
63	9/5	4.05.16	Virtual Memories			
64	10/5	5.05.16	Secondary Storage			
65	11/5	6.05.16	Secondary Storage			
66		7.05.16	REVISION			
67		11.05.16	REVISION			

Syllabus for Internal Assessment Tests (IAT) *

Sessional #	Syllabus
T1	Class # 01 - 32
T2	Class # 33 – 65
Improvement test	Important VTU Questions from Class # 01 - 65

*: See calendar of events for the schedules of IATs.

SEMESTER : IV -B
BRANCH : CSE
SUBJECT : Computer Organization
SUBJECT CODE : 10CS46
NO OF HRS/WK : 5

NAME OF THE FACULTY : V.Aishwaryalakshmi
DATE OF COMMENCEMENT : 18.01.2016
DATE OF CLOSING : 11.05.2016
CLASS STRENGTH : 66
TOTAL HRS : 65

Session No	Chapter no (No of hrs planed for the chapter)	DATE	Topics planned for the session	Teaching Aids	Assignments/ Tests planned for the chapter	Topics covered As per plan
1			Prerequisites	Chalk & Talk		
2			Prerequisites	”		
3			Prerequisites	”		
4	1/1	21.01.16	Computer Types, Historical Perspectives, Functional Units	”		
5	2/1	22.01.16	Basic operational concept, Performance Equation, Performance Measurement	”		
6	3/1	25.01.16	Numbers, Arithmetic Operations and Characters	”		

7	4/1	27.01.16	Memory Location & Addresses	“		
8	5/1	28.01.16	Instructions and Instruction Sequencing	”		
9	6/1	29.01.16	Instructions and Instruction Sequencing	”		
10	7/1	30.01.16	Instructions and Instruction Sequencing	”		
11	1/2	2.02.16	Addressing Mode	”		
12	2/2	3.02.16	Assembly language	”	Assignment- I	
13	3/2	4.02.16	Basic Input and Output Operations, Stacks and Queues	”		
14	4/2	5.02.16	Additional Instructions, Encoding of machine Instructions	”		
15	5/2	.02.16	Subroutines	“		
16	6/2	8.02.16	Subroutines	”		
17	1/3	10.02.16	Accessing I/O Devices	”		
18	2/3	11.02.16	Interrupts-Interrupt Hardware, Enabling and Disabling Interrupts	”		
19	3/3	12.02.16	Interrupts-Handling Multiple Devices	”	Assignment -II	
20	4/3	13.02.16	DMA	”		
21	5/3	15.02.16	Buses	”		
22	6/3	17.02.16	Buses	”		
23	7/3	18.02.16	Exceptions	“		
24	1/4	22.02.16	Interface Circuits	”		
25	2/4	23.02.16	Interface Circuits	”		
26	3/4	24.02.16	Interface Circuits	”		
27	4/4	26.02.16	Interface Circuits	”		
28	5/4	29.02.16	PCI bus	”		
29	6/4	1.03.16	PCI bus, SCSI bus	”		
30	7/4	2.03.16	SCSI bus, USB	”		
31	8/4	3.03.16	USB	”	Assignment –III	
32	9/4	5.03.16	USB	“		
33	1/6	8.03.16	Addition and Subtraction of Signed Numbers	”		
34	2/6	9.03.16	Design of fast adders	”		

35	3/6	10.03.16	Multiplication of positive numbers	”		
36	4/6	11.03.16	Signed operand multiplication,	”		
37	5/6	18.03.16	Fast multiplication	”		
38	6/6	19.03.16	Integer Division	”		
39	7/6	21.03.16	Integer Division	“		
40	8/6	22.03.16	Floating point number and operations	”		
41	9/6	23.03.16	Floating point number and operations	”	Assignm ent –IV	
42	1/8	28.03.16	Performance, The Power wall	”		
43	2/8	29.03.16	The switch from Uni-processor to multiprocessor, Amdahl’s law,	”		
44	3/8	30.03.16	Shared memory, Multiprocessors, Clusters, Message passing multiprocessors	”		
45	4/8	31.03.16	Hardware multithreading	”		
46	5/8	1.04.16	SISD,MIMD, SIMD, SPMD, Vector	”		
47	6/8	4.04.16	Sums	“		
48	1/7	5.04.16	Some fundamental concept	”		
49	2/7	6.04.16	Execution of complete instruction	”		
50	3/7	7.04.16	Execution of complete instruction	”		
51	4/7	11.04.16	Multiple bus organization	”		
52	5/7	13.04.16	Hard wired control	”		
53	6/7	15.04.16	Micro-programmed control	”		
54	7/7	16.04.16	Micro-programmed control	”		
55	1/5	18.04.16	Basic Concepts	“		
56	2/5	20.04.16	Semiconductor RAM Memories	”		
57	3/5	22.04.16	Read only memory, Speed, Size and Cost.	”		
58	4/5	23.04.16	Cache Memories-Mapping Function	”		
59	5/5	28.04.16	Replacement algorithms, cache-sums	”		

60	6/5	29.04.16	Performance Considerations	”		
61	7/5	30.04.16	Performance Considerations			
62	8/5	3.05.16	Virtual Memories			
63	9/5	4.05.16	Virtual Memories			
64	10/5	5.05.16	Secondary Storage			
65	11/5	6.05.16	Secondary Storage			
66		7.05.16	REVISION			
67		11.05.16	REVISION			

Syllabus for Internal Assessment Tests (IAT) *

Sessional #	Syllabus
T1	Class # 01 - 32
T2	Class # 33 – 65
T3	Class # 17 – 41

*: See calendar of events for the schedules of IATs.

SEMESTER : IV -C
BRANCH : CSE
SUBJECT : Computer Organization
SUBJECT CODE : 10CS46
NO OF HRS/WK : 5

NAME OF THE FACULTY : V.Aishwaryalakshmi
DATE OF COMMENCEMENT : 18.01.2015
DATE OF CLOSING : 11.05.2015
CLASS STRENGTH : 64
TOTAL HRS : 65

Session No	Chapter no (No of hrs planed for the chapter)	DATE	Topics planned for the session	Teaching Aids	Assignments/ Tests planned for the chapter	Topics covered As per plan
1			Prerequisites	Chalk & Talk		
2			Prerequisites	”		
3			Prerequisites	”		
4	1/1	21.01.16	Computer Types, Historical Perspectives, Functional Units	”		
5	2/1	23.01.16	Basic operational concept, Performance Equation, Performance Measurement	”		

6	3/1	25.01.16	Numbers, Arithmetic Operations and Characters	”		
7	4/1	27.01.16	Memory Location & Addresses	“		
8	5/1	28.01.16	Instructions and Instruction Sequencing	”		
9	6/1	29.01.16	Instructions and Instruction Sequencing	”		
10	7/1	1.02.16	Instructions and Instruction Sequencing	”		
11	1/2	2.02.16	Addressing Mode	”		
12	2/2	3.02.16	Assembly language	”	Assignment- I	
13	3/2	4.02.16	Basic Input and Output Operations, Stacks and Queues	”		
14	4/2	5.02.16	Additional Instructions, Encoding of machine Instructions	”		
15	5/2	9.02.16	Subroutines	“		
16	6/2	10.02.16	Subroutines	”		
17	1/3	11.02.16	Accessing I/O Devices	”		
18	2/3	12.02.16	Interrupts-Interrupt Hardware, Enabling and Disabling Interrupts	”		
19	3/3	13.02.16	Interrupts-Handling Multiple Devices	”	Assignment -II	
20	4/3	16.02.16	DMA	”		
21	5/3	17.02.16	Buses	”		
22	6/3	18.02.16	Buses	”		
23	7/3	22.02.16	Exceptions	“		
24	1/4	23.02.16	Interface Circuits	”		
25	2/4	25.02.16	Interface Circuits	”		
26	3/4	26.02.16	Interface Circuits	”		
27	4/4	29.02.16	Interface Circuits	”		
28	5/4	1.03.16	PCI bus	”		
29	6/4	2.03.16	PCI bus, SCSI bus	”		
30	7/4	4.03.16	SCSI bus, USB	”		
31	8/4	5.03.16	USB	”		
32	9/4	8.03.16	USB	“	Assignment –III	
33	1/6	9.03.16	Addition and Subtraction of Signed Numbers	”		

34	2/6	10.03.16	Design of fast adders	”		
35	3/6	17.03.16	Multiplication of positive numbers	”		
36	4/6	18.03.16	Signed operand multiplication,	”		
37	5/6	19.03.16	Fast multiplication	”		
38	6/6	21.03.16	Integer Division	”		
39	7/6	22.03.16	Integer Division	“		
40	8/6	24.03.16	Floating point number and operations	”		
41	9/6	28.03.16	Floating point number and operations	”	Assignment –IV	
42	1/8	29.03.16	Performance, The Power wall	”		
43	2/8	30.03.16	The switch from Uni-processor to multiprocessor, Amdahl’s law,	”		
44	3/8	31.03.16	Shared memory, Multiprocessors, Clusters, Message passing multiprocessors	”		
45	4/8	2.04.16	Hardware multithreading	”		
46	5/8	4.04.16	SISD, MIMD, SIMD, SPMD, Vector	”		
47	6/8	5.04.16	Sums	“		
48	1/7	6.04.16	Some fundamental concept	”		
49	2/7	7.04.16	Execution of complete instruction	”		
50	3/7	12.04.16	Execution of complete instruction	”		
51	4/7	13.04.16	Multiple bus organization	”		
52	5/7	15.04.16	Hard wired control	”		
53	6/7	16.04.16	Micro-programmed control	”		
54	7/7	18.04.16	Micro-programmed control	”		
55	1/5	21.04.16	Basic Concepts	“		
56	2/5	22.04.16	Semiconductor RAM Memories	”		
57	3/5	23.04.16	Read only memory, Speed, Size and Cost.	”		
58	4/5	28.04.16	Cache Memories-Mapping Function	”		

59	5/5	29.04.16	Replacement algorithms, cache-sums	”		
60	6/5	2.05.16	Performance Considerations	”		
61	7/5	3.05.16	Performance Considerations			
62	8/5	4.05.16	Virtual Memories			
63	9/5	5.05.16	Virtual Memories			
64	10/5	6.05.16	Secondary Storage			
65	11/5	10.05.16	Secondary Storage			
66		11.05.16	REVISION			
67		11.02.16	REVISION			

Syllabus for Internal Assessment Tests (IAT) *

Sessional #	Syllabus
T1	Class # 01 - 32
T2	Class # 33 – 65
T3	Class # 17 – 41

*: See calendar of events for the schedules of IATs.

Literature:

Book Type	Code	Author & Title	Publication info	
			Edition & Publisher	ISBN #
Text Book	TB1	Leland.L.Beck: System Software,	3 rd Edition, Pearson Education, 1997.	978-81-317-6460-2
Text Book	TB2	John.R.Levine, Tony Mason and Doug Brown: Lex and Yacc,	O'Reilly, SPD, 1998.	1565920007, 9781565920002
References	RB1	D.M.Dhamdhare: System Programming and Operating Systems	2 nd Edition, Tata McGraw - Hill, 1999.	1449335942