

DEPARTMENT OF ELECTRONICS & COMMUNICATION

Class : 4th Semester 'A' Section
Class Teacher : Ms. JYOTHI

Room No : CV 302
Session : JAN-MAY2016

Time Day	08:00 - 09:00	09:00 - 09:50	9:50 - 10:40	SHORT BREAK (10:40- 11:00)	11:10 - 12:00	12:00 - 12:50	LUNCH BREAK (12:50 – 13:30)	13:30 - 14:20	14:20 - 15:10	15:10 - 16:00
D1	MAT	LIC	LI		CS	A1 HDL LAB/ A2 MC LAB (VRD,BOO) (SVD,ABI)				
D2	HDL	MAT	MC		HDL	LIC		MAT	SS	MC
D3	A2 HDL LAB/ A3 MC LAB (JMR,BOO) (CTH,ABI)				MC	HDL		CS		LIC
D4	CS		MAT		SS					
D5	SS		LI		MAT	HDL		MAT	MC	LIC
D6	A3 HDL LAB/ A1 MC LAB (AVR,VRD) (SVD,NGS)				MC	HDL		LIC		SS

Sub Code	Subject	Faculty	Batch	Faculty 01	Faculty 02
10MAT41	Engg. Mathematics-IV	MS.UMA	LAB 01: Micro controller Lab		
10ES42	Microcontroller	MS.ABHILASHA	BATCH 1:	PROF.SURYA	PROF.NAGARATNA
10ES43	Control system	MR.MANAVALAN	BATCH 2:	PROF.SURYA	PROF.ABHILASHA
10EC44	signal & system	MS.BINEESH FATIMA	BATCH 3:	PROF.CHETHAN	PROF.ABHILASHA
10EC45	Fundamentals HDL	MS.JYOTHI	LAB 02: HDL Lab		
10EC46	linear IC's & Application	MR. SURYA	BATCH 1:	PROF.VEERENDER	PROF.BOOMIKA
10ESL47	Micro controller Lab		BATCH 2:	PROF.JYOTHI	PROF.BOOMIKA
10ESL48	HDL Lab		BATCH 3:	PROF.VEERENDER	PROF.AMMU

DEPARTMENT OF ELECTRONICS & COMMUNICATION

Class : 4 th Semester 'B' Section	Room No. : CV 303
Class Teacher : MR.SURYA	Session : 18/1/2015

Time Day	08:00 - 09:00	09:00 - 09:50	9:50 - 10:40	SHORT BREAK (10:40- 11:00)	11:10 - 12:00	12:00 - 12:50	LUNCH BREAK (12:50 – 13:30)	13:30 - 14:20	14:20 - 15:10	15:10 - 16:00	
D1	LIC	SS	MAT		MC	MAT		CS		HDL	
D2	CS		LI		MAT	HDL		B1 HDL LAB/ B2 MC LAB (BOO,JMR) (SVD,SDS)			
D3	CS		MAT		LIC	MC		LIC	SS		
D4	LIC	MAT	HDL		MC	MAT					
D5	B2 HDL LAB/ B3 MC LAB (BOO,JMR) (SVD,ABI)				LIC	MC		LIC	SS	HDL	
D6	HDL	MC	LI		SS			B3 HDL LAB/ B1 MC LAB (VRD,JMR) (ABI,SDS)			

Sub Code	Subject	Faculty	Batch	Faculty 01	Faculty 02
10MAT41	Engg. Mathematics-IV	MS.UMA	LAB-01 : Micro controller Lab		
10ES42	Microcontroller	MS.ABHILASHA	BATCH 1:	PROF.SRIDEVI	PROF.ABHILASHA
10ES43	Control system	MR.MANAVALAN	BATCH 2:	PROF.SURYA	PROF.SRIDEVI
10EC44	signal & system	MS.BINEESH FATIMA	BATCH 3:	PROF.SURYA	PROF.ABHILASHA
10EC45	Fundamentals HDL	MS.JYOTHI	LAB-02 : HDL Lab		
10EC46	linear IC's & Application	MR. SURYA	BATCH 1:	PROF.BOOMIKIA	PROF.JYOTHI
10ESL47	Micro controller Lab		BATCH 2:	PROF.BOOMIKIA	PROF.JYOTHI
10ESL48	HDL Lab		BATCH 3:	PROF.JYOTHI	PROF.VEERENDER

DEPARTMENT OF ELECTRONICS & COMMUNICATION

Class : 4TH Semester 'C' Section
Class Teacher : MS.PREETHI

Room No. : CV 304
Session : JAN-MAY 2016

Time Day	08:00 - 09:00	09:00 - 09:50	9:50 - 10:40	SHORT BREAK (10:40- 11:00)	11:10 - 12:00	12:00 - 12:50	LUNCH BREAK (12:50 - 13:30)	13:30 - 14:20	14:20 - 15:10	15:10 - 16:00
D1	C1 HDL LAB/C2 MC LAB (AVR,SHWS) (NGS,VNY)				HDL	CS		MAT	MC	SS
D2	MC	SS			LIC	MAT		HDL	CS	LIC
D3	LIC	MAT	LIC		MC	MAT		C2 HDL LAB/C3 MC LAB (SKH,SHWS) (SDS,VNY)		
D4	CS	HDL	LI		LIC	MC		C3 HDL LAB/C1 MC LAB (AVR,VRD) (NGS,SDS)		
D5	MAT	CS	MAT		SS	HDL		SS		CS
D6	MC	LI	HDL		CS	LIC				

Sub Code	Subject	Faculty	Batch	Faculty 01	Faculty 02
10MAT41	Engg. Mathematics-IV	MS.UMA	LAB-01 : Micro controller Lab		
10ES42	Microcontroller	MS.PRIYA	BATCH 1:	PROF.SRIDEVI	PROF.NAGARATNA
10ES43	Control system	MS.PREETHI	BATCH 2:	PROF.NAGARATNA	PROF.VINAY
10EC44	signal & system	MR.MAHESH	BATCH 3:	PROF.SRIDEVI	PROF.VINAY
10EC45	Fundamentals HDL	MR.MAHESH.V	LAB-02 : HDL Lab		
10EC46	linear IC's & Application	MS.PALLAVI	BATCH 1:	PROF. AMMU	PROF.SHWETHA
10ESL47	Micro controller Lab		BATCH 2:	PROF.SUNIL KUMAR	PROF.SHWETHA
10ESL48	HDL Lab		BATCH 3:	PROF. AMMU	PROF.VEERENDER

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HOD (ECE)

PRINCIPAL

DEPARTMENT OF ELECTRONICS & COMMUNICATION

Class: 4th Semester 'D' Section

Room no: TC 405

Class Teacher: Ms.POORNIMA

Session: JAN-MAY 2016

Time Day	08:00 - 09:00	09:00 - 09:50	9:50 - 10:40	SHORT BREAK (10:40- 11:00)	11:10 - 12:00	12:00 - 12:50	LUNCH BREAK (12:50 – 13:30)	13:30 - 14:20	14:20 - 15:10	15:10 - 16:00	
D1	LIC	MC			SS	CS		MAT	CS	SS	
D2	D1 HDL LAB/D2 MC LAB (SHWS,SKH) (CTH,VNY)				LIC	MC		SS	MAT	LIC	
D3	CS	MAT	LIC		MC	HDL		MAT	CS	SS	
D4	MAT	HDL	SS		D2 HDL LAB/D3 MC LAB (AVR,SKH) (CTH,NGS)						
D5	SS	LIC	LI		HDL	CS		LIC	MC		
D6	MAT	HDL	LI		D3 HDL LAB/D1 MC LAB (SHWS,SKH) (CTH,VNY)				CS	HDL	

Sub Code	Subject	Faculty	Batch	Faculty 01	Faculty 02
10MAT41	Engg. Mathematics-IV	MR.KAMAL KUMAR	LAB-01 : Micro controller Lab		
10ES42	Microcontroller	MS.MEENAKSHI	BATCH 1:	PROF.CHETHAN	PROF.VINAY
10ES43	Control system	MS.SUGANYA	BATCH 2:	PROF.CHETHAN	PROF.VINAY
10EC44	signal & system	MS.AMMU	BATCH 3:	PROF.CHETHAN	PROF.NAGARATNA
10EC45	Fundamentals HDL	MR.SUNIL KUMAR	LAB-02 : HDL Lab		
10EC46	linear IC's & Application	MS.POORNIMA	BATCH 1:	PROF.SHWETHA	PROF.SUNIL KUMAR
10ESL47	Micro controller Lab		BATCH 2:	PROF.AMMU	PROF.SUNIL KUMAR
10ESL48	HDL Lab		BATCH 3:	PROF.SHWETHA	PROF.SUNIL KUMAR

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