

DEPARTMENT OF TELECOMMUNICATION ENGINEERING

Class : 4th Semester 'A' Section (TENTATIVE TIME TABLE)

Room No. : LH-403

Class Teacher : Ms. Priya

Session : 18 JAN-23 JAN 2016

Time Day	08:00 - 09:00	09:00 - 09:50	9:50 - 10:40	SHORT BREAK (10:40- 11:00)	11:10 - 12:00	12:00 - 12:50	LUNCH BREAK (12:50 - 13:30)	13:30 - 14:20	14:20 - 15:10	15:10 - 16:00
D1	SS	MAT	MC		HDL	LIC		MC lab/HDL lab		
D2	LIC	MAT	MC		HDL	SS		MC lab/HDL lab		
D3	SS	MC	LIC		MAT	HDL		MC lab/HDL lab		
D4	HDL	CS	CS		SS	LIC				
D5	CS	CS	MC		MAT	LIC		MC lab/HDL lab		
D6	HDL	CS	MAT		MC	SS		MC lab/HDL lab		

Sub Code	Subject	Faculty	Batch	Faculty 01	Faculty 02
10MAT41	Engineering Mathematics IV (M-IV)	Mrs Revathi	LAB-01 : Microcontroller Lab (MC LAB)		
10ES42	Microcontroller(MC)	Ms Priya	A1	Mrs. Meenakshi	Mrs. Pallavi
10ES43	Control System(CS)	Mr.Manawalan G	A2	Ms. Priya	Mrs. Meenakshi
10EC44	Signals and Systems (SS)	Mr. Mahesh Kumar	A3	Mrs. Pallavi	Ms. Priya
10EC45	Fundamental of HDL (HDL)	Mr. Mahesh V	LAB-02 : HDL Lab (LD LAB)		
10EC46	Linear Integrated Circuits(LIC)	Mrs.Pallavi	A1	Mr. Mahesh Kumar	Mrs. Alka
10ESL47	MC LAB		A2	Mrs. Alka	Mr. Mahesh V
10ECL48	HDL LAB		A3	Mr. Mahesh Kumar	Mr. Mahesh V

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CTTO

HOD (TC)

PRINCIPAL

DEPARTMENT OF TELECOMMUNICATION ENGINEERING

Class : 4th Semester 'B' Section

Room No. : LH-404

Class Teacher : MEENAKSHI

Session : JAN 2016 – MAY 2016

Time Day	08:00 - 09:00	09:00 - 09:50	9:50 - 10:40	SHORT BREAK (10:40- 11:00)	11:10 - 12:00	12:00 - 12:50	LUNCH BREAK (12:50 – 13:30)	13:30 - 14:20	14:20 - 15:10	15:10 - 16:00
D1	HDL	CS	CS		LIC	MC		HDL	SS	MAT
D2	SS	SS	LI(HDL)		MAT	HDL		MC lab (B1)/HDL lab (B2)		
D3	SS	LIC	SS		CS	CS		MC	MAT	LIC
D4	MC	MAT	HDL		LI(MC)	LIC				
D5	MC lab(B2)/HDL lab(B3)				MAT	SS		HDL	LIC	CS
D6	CS	MC	MC		MAT	LIC		MC lab(B3)/HDL lab(B1)		

Sub Code	Subject	Faculty	Batch	Faculty 01	Faculty 02
10MAT41	Engineering Mathematics IV (M-IV)	Ms Revathi	LAB-01 : Microcontroller Lab (MC LAB)		
10ES42	Microcontroller(MC)	Mrs Meenakshi	B1	Mrs. Meenakshi	Ms. Priya
10ES43	Control System(CS)	Mrs. Suganya (ECE)	B2	Ms. Priya	Mrs. Pallavi
10EC44	Signals and Systems (SS)	Mrs. Ammu (ECE)	B3	Mrs. Pallavi	Mrs. Meenakshi
10EC45	Fundamental of HDL (HDL)	Mr Sunil(ECE)	LAB-02 : HDL Lab (LD LAB)		
10EC46	Linear Integrated Circuits(LIC)	Mrs.Poornima(ECE)	B1	Mr. Mahesh V	Mrs. Alka
10ESL47	MC LAB	Mrs. Meenakshi, Priya, Mrs. Pallavi	B2	Mr. Mahesh Kumar	Mrs ALKA
10ECL48	HDL LAB	Mr. Mahesh Kumar, Mahesh V, Mrs. Alka	B3	Mr. Mahesh V	Mr. Mahesh Kumar

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CTTO

HOD (TC)

PRINCIPAL