

**DEPARTMENT OF TELECOMMUNICATION ENGINEERING**

**Class : 6<sup>th</sup> Semester 'A' Section**

**Room No. : LH-406**

**Class Teacher : Mrs. Shweta**

**Session : JAN 2016 – MAY 2016**

Time Day	08:00 - 09:00	09:00 - 09:50	9:50 - 10:40	SHORT BREAK (10:40- 11:00)	11:10 - 12:00	12:00 - 12:50	LUNCH BREAK (12:50 – 13:30)	13:30 - 14:20	14:20 - 15:10	15:10 - 16:00	
D1	AWP	C++/DSDV	MP		MWR	ITC		MP	AWP	DC	
D2	DC	ITC	AWP		MP	LI(MWR)		MWR LAB (A1)/MP LAB (A2)			
D3	C++/DSDV	MWR	MP		DC	DC		AWP	ITC	MWR	
D4	MWR LAB (A2)/MP LAB (A3)				LI(MP)	C++/DSDV					
D5	MWR LAB (A3)/MP LAB (A1)				ITC	MP		C++/DSDV	MWR	AWP	
D6	DC	ITC	C++/DSDV		MWR	AWP		DC	MP		

Sub Code	Subject	Faculty	Batch	Faculty 01	Faculty 02
10TE61	Digital Communication(DC)	Mrs Vidya T V	<b>LAB-01 : Microprocessor (MP LAB)</b>		
10TE62	Microprocessor(MP)	Mrs. Suma	A1	Mrs. Sophiya	Mr. Harsha
10TE63	Antenna and propagation (AWP)	Mrs. Sutapa	A2	Mrs. Priti	Mr. Harsha
10TE64	Microwave and Radar(MWR)	Prof.KVS Anandababu	A3	Mrs. Sophiya	Mr. Harsha
10TE65	Information Theory and Coding(ITC)	Mr. Harsha	<b>LAB-02 : Microwave and Antenna Lab (MWR LAB)</b>		
10EC661 /10EC666	PROGRAMMING IN C++/ DIGITAL SYSTEM DESIGN USING VERILOG	Mrs. Poonam/Mrs.Shweta Sharma	A1	Ninni, Ramesh	Laxmi
10TEL67	Microwave and Antenna Lab		A2	Ramesh, Mr. Abhishek	Laxmi
10TEL68	MICROPROCESSOR LAB		A3	Mr. Abhishek, Ninni,	Ramesh

TTO

CTTO

HOD (TC)

PRINCIPAL

**DEPARTMENT OF TELECOMMUNICATION ENGINEERING**

**Class : 6<sup>th</sup> Semester 'B' Section**

**Room No. : LH-407**

**Class Teacher : Mr. Abhishek**

**Session : JAN 2016 – MAY 2016**

Time Day	08:00 - 09:00	09:00 - 09:50	9:50 - 10:40	SHORT BREAK (10:40- 11:00)	11:10 - 12:00	12:00 - 12:50	LUNCH BREAK (12:50 – 13:30)	13:30 - 14:20	14:20 - 15:10	15:10 - 16:00
D1	AWP	C++/DSDV	MWR		DC	MP		MWR LAB (B1)/MP LAB (B2)		
D2	MP	DC	LI(MWR)		LI(MP)	ITC		MWR	AWP	AWP
D3	C++/DSDV	ITC	MP		AWP	MP		MWR LAB (B2)/MP LAB (B3)		
D4	DC	DC	MWR		MP	C++/DSDV				
D5	DC	DC	MWR		AWP	ITC		C++/DSDV	ITC	
D6	ITC	MWR	C++/DSDV		MP	AWP		MWR LAB (B3)/MP LAB (B1)		

Sub Code	Subject	Faculty	Batch	Faculty 01	Faculty 02
10TE61	Digital Communication(DC)	Mr Raveesh	<b>LAB-01 : Microprocessor (MP LAB)</b>		
10TE62	Microprocessor(MP)	Mrs. Sophiya	B1	Mrs. Sophiya	Mr. Priti
10TE63	Antenna and propagation (AWP)	Mr. Abhishek	B2	Mrs. Priti	Mr. Harsha
10TE64	Microwave and Radar(MWR)	Prof.KVS Anandababu	B3	Mrs. Sophiya	Mrs. Priti
10TE65	Information Theory and Coding(ITC)	Mr. Harsha	<b>LAB-02 : Microwave and Antenna Lab (MWR LAB)</b>		
10EC661 /10EC666	PROGRAMMING IN C++/ DIGITAL SYSTEM DESIGN USING VERILOG	Mrs. Poonam/Mrs.Shweta Sharma	B1	Ramesh, Mr. Abhishek	Laxmi
10TEL67	Microwave and Antenna Lab		B2	Mr. Abhishek, Ninni,	Ramesh
10TEL68	MICROPROCESSOR LAB		B3	Ninni, Ramesh	Laxmi

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