

DEPARTMENT OF ELECTRONICS & COMMUNICATION

Class : 2ND Semester M.TECH VLSI Class Teacher : MR.CHETAN	Room No. : CV 303 Session : FEB-MAY2016
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Time Day	08:00 - 09:00	09:00 - 09:50	9:50 - 10:40		11:10 - 12:00	12:00 - 12:50		13:30 - 14:20	14:20 - 15:10	15:10 - 16:00
D1	AAMV (CTH)		RTOS (SUD)	SHORT BREAK (10:40- 11:00)	LPV (CTH)	SOC (BOO)	LUNCH BREAK (12:50 - 13:30)			
D2	VLSITV (BOO)				LPV (CTH)	LPV (CTH)		LAB (VNY, MGR)		
D3	RTOS (SUD)				AAMV (CTH)	SOC (BOO)				
D4	VLSITV (BOO)	LPV (CTH)	SOC (BOO)							
D5	AAMV(CTH)		SOC (BOO)		LPV (CTH)	VLSITV (BOO)		LAB (VNY, MGR)		
D6	RTOS (SUD)	VLSITV (BOO)	SOC (BOO)		RTOS (SUD)					

Sub Code	Subject	Faculty	Batch	Faculty 01	Faculty 02
14EVE21	Design of Analog and mixed mode vlsi	MR. CHETAN	LAB-01 : LAB		
14EVE22	Low power vlsi	MR. CHETAN	BATCH :	PROF.MAHESH S. GOUR	PROF.VINAY
14EVE23	Vlsi testing and verification	MS. BOOMIKIA			
14EVE25	System on chip	MS. BOOMIKIA			
14ELD24	Real Time Operating Systems	MS.SUDATTA M.			

TTO

CTTO

HOD (ECE)

PRINCIPAL

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