

DEPARTMENT OF ELECTRONICS & COMMUNICATION

Class : Digital Electronics 4th SEM Room No. : 302
Session : FEB-MAY 2016

Time Day	08:00 - 09:00	09:00 - 09:50	9:50 - 10:40	SHORT BREAK (10:40- 11:00)	11:10 - 12:00	12:00 - 12:50	LUNCH BREAK (12:50 - 13:30)	13:30 - 14:20	14:20 - 15:10	15:10 - 16:00
D1	ACA	AVLSI	AVLSI		ACA	ACA				
D2	AVLSI	ACA	ACA		AVLSI	AVLSI				
D3										
D4										
D5										
D6										

Sub Code	Subject	Faculty	Batch	Faculty 01	Faculty 02
14EVE41	SYNTHESIS AND OPTIMIZATION OF DIGITAL CIRCUITS	Prof. RES			
14EVE421	ADVANCE IN VLSI DESIGN	Prof. MAHESH G.			