

**DEPARTMENT OF ELECTRONICS & COMMUNICATION**

**Class : VLSI DESIGN AND EMBEDDED SYSTEM 4<sup>th</sup> SEM**

**Room No. : 301**

**Session : FEB-MAY 2016**

| Time<br>Day | 08:00 -<br>09:00 | 09:00 -<br>09:50 | 9:50 -<br>10:40 | SHORT BREAK<br>(10:40- 11:00) | 11:10 -<br>12:00 | 12:00 -<br>12:50 | LUNCH BREAK<br>(12:50 – 13:30) | 13:30 -<br>14:20 | 14:20 -<br>15:10 | 15:10 -<br>16:00 |
|-------------|------------------|------------------|-----------------|-------------------------------|------------------|------------------|--------------------------------|------------------|------------------|------------------|
| D1          | SODC             | AVLSI            | AVLSI           |                               | SODC             | SODC             |                                |                  |                  |                  |
| D2          | AVLSI            | SODC             | SODC            |                               | AVLSI            | AVLSI            |                                |                  |                  |                  |
| D3          |                  |                  |                 |                               |                  |                  |                                |                  |                  |                  |
| D4          |                  |                  |                 |                               |                  |                  |                                |                  |                  |                  |
| D5          |                  |                  |                 |                               |                  |                  |                                |                  |                  |                  |
| D6          |                  |                  |                 |                               |                  |                  |                                |                  |                  |                  |

| Sub Code | Subject  | Faculty         | Batch | Faculty 01 | Faculty 02 |
|----------|--|-----------------|-------|------------|------------|
| 14EVE41  | SYNTHESIS AND OPTIMIZATION OF DIGITAL CIRCUITS | Prof. MAHESH G. |       |            |            |
| 14EVE421 | ADVANCE IN VLSI DESIGN                         | Prof. MAHESH G. |       |            |            |