

**CMR INSTITUTE  
OF TECHNOLOGY**



Session wise – Course Plan

**Department of Mathematics**

SEMESTER : III NAME OF THE FACULTY : Dr.Sunanda Saha  
BRANCH : ECE DATE OF COMMENCEMENT : 28.07.2016  
SUBJECT : ENGG MATHS III DATE OF CLOSING : 21.11.2016  
SUBJECT CODE : 15MAT31 CLASS STRENGTH : 180  
NO OF HRS/WK : 6 TOTAL HOURS : 64

Sessi on No	Chapter no (No of hrs planned for the chapter)	DATE	Topics planned for the session	Teaching Aids	Assignments/ Tests planned for the chapter	Topi cs cov ered  As per plan
1	1/5	28/07/2016	<b>Module 5 Vector Integration-</b> Introduction to vectors	"		
2	2/5	29/07/2016	Introduction to vectors	"		
3	3/5	1/8/2016	Line Integral	"		
4	4/5	2/8/2016	Problems on Line Integral	"		
5	5/5	3/8/2016	Green's theorem and proof	"		

6	6/5	5/8/2016	Problems on Green's theorem	„		
7	7/5	6/8/2016	Surface Integral Problem	„	Assignment- I	
8	8/5	8/8/2016	Surface Integral Problem	„		
9	9/5	9/8/2016	Class Test on assignment problem	„	Test I	
10	10/5	10/8/2016	Divergence theorem and its proof	“		
11	11/5	11/8/2016	Problems on Divergence theorem	„		
12	12/5	12/8/2016	Problems on Divergence and Stoke's theorem	„		
13	13/5	16/8/2016	Problems on Stoke's theorem	„		
14	1/1	17/8/2016	<b>Introduction to Fourier series Module 1</b>	„		
15	2/1	18/8/2016	Problems on Fourier series	„		
16	3/1	19/8/2016	Problems on Fourier series	„		
17	4/1	20/8/2016	Problems on Fourier series	“	Assignment –II	
18	5/1	22/8/2016	Problems on Fourier series	“		
19	6/1	23/8/2016	Half range Fourier series	“		
20	7/1	24/8/2016	Half range Fourier series	„		
21	8/1	25/8/2016	Harmonic Analysis	„		
22	9/1	26/8/2016	Harmonic Analysis	„		

23	1/2	27/8/2016	<b>Module 2 Fourier Transform</b>	„	Test II	
24	2/2	30/8/2016	Problems on Fourier Transform	„	Assignment –III	
25	3/2	31/8/2016	Problems on Fourier Transform	“		
26	4/2	1/9/2016	Revision for IAT-1	“		
27	14/5	9/9/2016	<b>Module 5 Calculus of variations</b> variation of function and functional	„		
28	16/5	10/9/2016	Calculus of variations variation of function and functional	„	Assignment –IV	
29	17/5	13/9/2016	Calculus of variations variation of function and functional	„		
30	18/5	14/9/2016	Euler’s equation	„		
31	19/5	16/9/2016	Euler’s equation	„		
32	20/5	19/9/2016	Geodesics	„	Test III	
33	21/5	21/9/2016	Geodesics	„		
34	22/5	23/9/2016	Minimal surface revolution	„		
35	23/5	23/9/2016	Hanging chain problem	„		
36	1/3	24/9/2016	<b>Numerical Methods - Regula-Falsi Method</b>	„		
37	2/3	26/9/2016	Newton-Raphson method.	„		
38	1/4	27/9/2016	<b>Module 4 Finite Differences</b> Forward and backward differences	“	Assignment –V	
39	2/4	28/9/2016	Newton’s forward and backward interpolation	„		

			formulae			
40	3/4	29/9/2016	Newton's forward and backward interpolation formulae	„		
41	4/4	3/10/2016	Newton's divided difference formula	„		
42	5/4	4/10/2016	Lagrange's interpolation formula	„	Assignment –VI	
43	6/4	5/10/2016	Inverse interpolation formula	„		
44	7/4	6/10/2016	Numerical integration Simpson's 1/3 rule	„		
45	8/4	6/10/16	Numerical integration Simpson's 3/8 rule	„		
46	9/4	7/10/2016	Numerical integration Weddle's rule	„		
47	3/3	7/10/2016	<b>Module 3-</b> Review of measures of central tendency and dispersion.	„	Test IV	
48	4/3	8/10/2016	Correlation-Karl Pearson's coefficient of correlation-problems.	„		
49	5/3	13/10/2016	Regression analysis- lines of regression - problems	„	Assignment – VII	
50	6/3	17/10/2016	Curve fitting by the method of least squares	„		
51	7/3	18/10/2016	Curve fitting by the method of least squares	„		
52	8/3	19/10/2016	Curve fitting by the method of least squares	„		
53	4/2	21/10/2016	<b>Module 2</b> Z transform-	„		

			definition, standard forms			
54	5/2	22/10/2016	Z transform: difference equations	“		
55	6/2	27/10/2016	Damping rule, shifting rule, initial value theorem, final value theorem	„	Assignment - VIII	
56	7/2	27/10/2016	Damping rule, shifting rule, initial value theorem, final value theorem	„		
57	8/2	28/10/2016	Damping rule, shifting rule, initial value theorem, final value theorem	„		
58	9/2	28/10/2016	Inverse Z-Transform. Applications to solve difference equations	“	Test VI	
59	10/2	2/11/2016	Inverse Z-Transform. Applications to solve difference equations	„		
60	15/5	3/11/2016	Revision	Board/Chalk		
61	16/5	4/11/2016	Revision	„		
62	20/5	5/11/2016	Revision	„		
63	22/5	7/11/2016	Revision	„		
64	24/5	8/11/2016	Revision	“		

Signature of faculty

Signature of HOD

Signature of Principal



**Department of Electronics & Communication**

SEMESTER : III NAME OF THE FACULTY : Mr. SURYA VARCHASVI.D.  
BRANCH : ECE DATE OF COMMENCEMENT : 28.07.2016  
SUBJECT : AE DATE OF CLOSING : 09.11.2016  
SUBJECT CODE : 15EC32 CLASS STRENGTH : 107( A & B Section)  
NO OF HRS/WK : 6 TOTAL HRS : 69

Session No	Chapter no (No of hrs planed for the chapter)	DATE	Topics planned for the session	Teaching Aids	Assignments/ Tests planned for the chapter	Topics covered As per plan
1	<b>Prerequisite</b>	01/08/2016	Fundamentals of Electric Circuits	Board, Chalk, Duster		
2	<b>Prerequisite</b>	2/8/2016	Diode Piecewise modeling.	“		
3	<b>Prerequisite</b>	3/8/2016	Linearity and Superposition.	“		
4	<b>Prerequisite</b>	4/8/2016	DC Transistor Circuits	“		
5	<b>Prerequisite</b>	5/8/2016	Diode Small Signal modeling	“		
6	<b>Prerequisite</b>	6/8/2016	Large Signal Model of Transistors	“	Prerequisite Assignment	
7	<b>1/1</b>	8/8/2016	<b>Module 1: BJT AC Analysis</b> Basics- BJT configurations	Board, Chalk, Duster		
8	<b>2/1</b>	9/8/2016	BJT- DC biasing, fixed bias (pre-requisite)	“		
9	<b>3/1</b>	10/8/2016	Emitter and voltage divider bias	“		

10	4/1	11/8/2016	BJT Transistor Modeling, The re transistor model,	“		
11	5/1	12/8/2016	Common emitter fixed bias, Voltage divider bias,	“		
12	6/1	13/8/2016	Emitter follower configuration. Darlington connection-DC bias;	“	Assignment-1	
13	7/1	16/8/2016	The Hybrid equivalent model,	“		
14	8/1	17/8/2016	Approximate Hybrid Equivalent Circuit-	“		
15	9/1	18/8/2016	Fixed bias, Voltage divider, Emitter follower configuration;	“		
16	10/1	19/8/2016	Complete Hybrid equivalent model	“		
17	11/1	20/8/2016	Hybrid $\pi$ Model.	”		
18	12/1	22/8/2016	Problems and revision	Board, Chalk, Duster		
19	13/1	23/8/2016	Problems and revision	“		
20	1/2	24/8/2016	<b>Module 2: Field Effect Transistors</b> Pre- requisites	“		
21	2/2	25/8/2016	Construction and Characteristics of JFETs,	“		
22	3/2	26/8/2016	Transfer Characteristics,	”		
23	4/2	27/8/2016	Depletion type MOSFET, Enhancement type MOSFET.			
24	5/2	29/8/2016	<b>FET Amplifiers:</b> JFET small signal model,	“		
25	6/2	30/8/2016	Fixed bias configuration, Self bias configuration,	“	Assignment-2	
26	7/2	31/8/2016	Voltage divider configuration,	“		
27	8/2	1/9/2016	Common Gate configuration.	“		
28	9/2	2/9/2016	Source-Follower Configuration,	“		
29	10/2	9/9/2016	Cascade configuration.	“		



30	<b>11/2</b>	10/9/2016	Problems & revision	“		
31	<b>12/2</b>	13/9/2016	Problems & revision	“		
32	<b>1/3</b>	14/9/2016	<b>Module 3: BJT and JFET Frequency Response:</b> Pre- requisites	“		
33	<b>2/3</b>	15/9/2016	Logarithms, Decibels,	“		
34	<b>3/3</b>	16/9/2016	Low frequency response – BJT Amplifier with RL	“		
35	<b>4/3</b>	17/9/2016	, Low frequency response- FET Amplifier	“		
36	<b>5/3</b>	19/9/2016	Miller effect capacitance	“	Assignm ent-3	
37	<b>6/3</b>	20/9/2016	High frequency response – BJT Amplifier,	“		
38	<b>7/3</b>	21/9/2016	High frequency response-FET Amplifier	“		
39	<b>8/3</b>	22/9/2016	Multistage Frequency Effects.	“		
40	<b>9/3</b>	23/9/2016	Problems & revision	“		
41	<b>10/3</b>	24/9/2016	Problems & revision	“		
42	<b>1/4</b>	26/9/2016	<b>Module 4:Feedback and Oscillator Circuits:</b> Pre- requisites	“		
43	<b>2/4</b>	27/9/2016	Feedback concepts,	“		
44	<b>3/4</b>	28/9/2016		“		
45	<b>4/4</b>	29/9/2016	Feedback connection types	“		
46	<b>5/4</b>	3/10/2016		“		
47	<b>6/4</b>	4/10/2016	Practical feedback circuits	“		
48	<b>7/4</b>	5/10/2016	Oscillator operation	Board, Chalk, Duster	Assignm ent-4	
49	<b>8/4</b>	6/10/2016	FET Phase shift oscillator	“		
50	<b>9/4</b>	7/10/2016	Wein bridge oscillator	“		
51	<b>10/4</b>	8/10/2016	Tuned Oscillator circuit	“		

52	<b>11/4</b>	13/10/2016		“		
53	<b>12/4</b>	14/10/2016	Crystal oscillator, UJT Construction UJT Oscillator	“		
54	<b>13/4</b>	17/10/2016	Problems and revision Problems and revision	“		
55	<b>14/4</b>	18/10/2016		“		
56	<b>1/5</b>	19/10/2016	<b>Module 5:Power Amplifiers:</b> Pre- requisites	“		
57	<b>2/5</b>	20/10/2016	Definition and amplifier types, Series fed class A amplifier	“		
58	<b>3/5</b>	21/10/2016		“		
59	<b>4/5</b>	22/10/2016	Transformer coupled class A amplifier	“		
60	<b>5/5</b>	27/10/2016	Class B amplifier operation and circuits	“	Assignm ent-5	
61	<b>6/5</b>	28/10/2016		“		
62	<b>7/5</b>	1/11/2016	Amplifier distortion	“		
63	<b>8/5</b>	2/11/2016	Class C and Class D amplifiers	“		
64	<b>9/5</b>	3/11/2016		“		
65	<b>10/5</b>	4/11/2016	<b>Voltage regulators:</b> Discrete transistor voltage regulation	“		
66	<b>11/5</b>	5/11/2016	Series and Shunt Voltage regulators	“		
67	<b>12/5</b>	7/11/2016	Revision for module 5	“		
68	<b>revision</b>	8/11/2016	Revision for improvement test	“		
69	<b>revision</b>	9/11/2016	Revision for improvement test	“		

Signature of faculty

Signature of HOD

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**CMR INSTITUTE OF TECHNOLOGY**

Session wise – Course Plan



**Department of Electronics and Communication**

SEMESTER : III  
SECTIONS : A, B  
SUBJECT : Digital Electronics  
SUBJECT CODE : 15EC33  
NO OF HRS/WK : 5 hrs

NAME OF THE FACULTY : Abhilasha Saksena  
DATE OF COMMENCEMENT : 25/ 08/ 2016  
DATE OF CLOSING : 19/ 11/ 2016  
CLASS STRENGTH : 55/ 56  
TOTAL HRS : 60 hrs

Session No	Day	Chapter no (No of hrs planed for the topic)	Date	Topics planned for the session	Teaching Aids	Assignments / Tests planned for the chapter	Topics covered As per plan
1	3	1/3	27.07.16 Wednesday	Number System, Binary Arithmetic Operations	Board, chalk, duster		
2	4	2/3	28.07.16 Thursday	Code conversions, Basic Logic	„		
3	5	3/3	29.07.16 Friday	Basic & Universal Logic Gates, Truth Table, Boolean Expression	„		
4	6	1/10	30.07.16 Saturday	Basic Boolean Laws & simplification of expressions – Minterms & Maxterms	„	Assignment 1	
5	1	2/10	01.08.16 Monday	Combinational expression – POS & SOP, K-Maps,,	„		
6	2	3/10	02.08.16 Tuesday	„	„		
7	3	4/10	03.08.16 Wednesday	„	„		
8	4	5/10	04.08.16 Thursday	„	„		
9	5	6/10	05.08.16 Friday	„	„		
10	6	7/10	06.08.16 Saturday	„	„		
11	1	8/10	08.08.16 Monday	Combinational expression – POS & SOP, K-Maps with Don't Care Terms	„	Assignment 2	
12	2	9/10	09.08.16 Tuesday	„	„		

13	3	10/10	10.08.16 Wednesday	„	„		
14	4	1/8	11.08.16 Thursday	„	„		
15	5	2/8	12.08.16 Friday	„	„		
16	6	3/8	16.08.16 Tuesday	„	„		
17	1	4/8	17.08.16 Wednesday	„	„		
18	2	5/8	18.08.16 Thursday	„	„		
19	3	6/8	19.08.16 Friday	Quine-McClusky Minimization Technique	„	Assignment 3	
20	4	7/8	20.08.16 Saturday	„	„		
21	5	8/8	22.08.16 Monday	„	„		
22	6	1/6	23.08.16 Tuesday	„	„		
23	1	2/6	24.08.16 Wednesday	Quine-McClusky Minimization Technique Maps – reduced prime implicants table	„	Assignment 4	
24	2	3/6	25.08.16 Thursday	„	„		
25	3	4/6	26.08.16 Friday	„	„		
26	4	5/6	27.08.16 Saturday	„	„		
27	5	6/6	29.08.16 Monday	„	„		
28	6	1/4	30.08.16 Tuesday	„	„		
29	1	2/4	31.08.16 Wednesday	„	„		
30	2	3/4	01.09.16 Thursday	„	„		
31	3	4/4	02.09.16 Friday	„	„		
32			06.09.16 Tuesday	IAT 1	„		
33			07.09.16 Wednesday	IAT 1	„		
34			08.09.16 Thursday	IAT 1	„		
35	4	1/3	09.09.16 Friday	Decoders Various circuits using decoder	„		

36	5	2/3	10.09.16 Saturday	„	„		
37	6	3/3	13.09.16 Tuesday	„	„		
38	1	1/9	14.09.16 Wednesday	Multiplexers & Digital circuits using MUX	„	Assignment 5	
39	2	2/9	15.09.16 Thursday	„	„		
40	3	3/9	16.09.16 Friday	„	„		
41	4	4/9	17.09.16 Saturday	SR Latch & Application	„		
42	5	5/9	19.09.16 Monday	Switch Denounce	„	Assignment 6	
43	6	6/9	20.09.16 Tuesday	„	„		
44	1	7/9	21.09.16 Wednesday	„	„		
45	2	8/9	22.09.16 Thursday	The gated SR Latch, J K Flip Flop, Master Slave, Edge Triggered Flip Flop	„	Assignment 7	
46	3	9/9	23.09.16 Friday	Registers, Counters	„		
47	4	1/13	24.09.16 Saturday	„	„		
48	5	2/13	26.09.16 Monday	Binary Ripple Counter	„	Assignment 8	
49	6	3/13	27.09.16 Tuesday	„	„		
50	1	4/13	28.09.16 Wednesday	„	„		
51	2	5/13	29.09.16 Thursday	Synchronous Counter	„	Assignment 9	
52	3	6/13	03.10.16 Monday	„	„		
53	4	7/13	04.10.16 Tuesday	„	„		
54	5	8/13	05.10.16 Wednesday	Asynchronous Counter	„	Assignment 10	
55	6	9/13	06.10.16 Thursday	„	„		
56	1	10/13	07.10.16 Friday	„	„		
57	2	11/13	08.10.16 Saturday	Mealy and Moore Models	„	Assignment 11	
58	3	12/13	13.10.16 Thursday	“	„		

59	4	13/13	14.10.16 Friday	“	”		
60			17.10.16 Monday	IAT2	”		
61			18.10.16 Tuesday	IAT2	”		
62			19.10.16 Wednesday	IAT2	”		
63	5	1/8	20.10.16 Thursday	State Machine Notation	”	Assignment 12	
64	6	2/8	21.10.16 Friday	“	”		
65	1	3/8	22.10.16 Saturday	Synchronous Sequential Circuit Analysis and Design	”		
66	2	4/8	24.10.16 Monday	”	”		
67	3	5/8	25.10.16 Tuesday	”	”		
68	4	6/8	26.10.16 Wednesday	”	”		
69	5	7/8	27.10.16 Thursday	Construction of state Diagrams, Counter Design “	”	Assignment 13	
70	6	1/8	28.10.16 Friday	”	”		
71	1	2/8	02.11.16 Wednesday	”	”		
72	2	3/8	03.11.16 Thursday	”	”		
73	3	4/8	04.11.16 Friday	”	”		
74	4	5/8	05.11.16 Saturday	”	”		
75	5	6/8	07.11.16 Monday	Revision	”	Assignment 14	
76	6	7/8	08.11.16 Tuesday	“	”		
77	1	8/8	09.11.16 Wednesday	“	”		
78			10.11.16 Thursday	<b>IMPROVEMENT TEST</b>	”		
79			11.11.16 Friday	<b>IMPROVEMENT TEST</b>	”		
80			12.11.16 Saturday	<b>IMPROVEMENT TEST</b>	”		

Signature of faculty

Signature of HOD

Signature of Principal

**Department of Telecommunication**

SEMESTER : III  
BRANCH : ECE  
SUBJECT : NA  
SUBJECT CODE : 15EC34  
NO OF HRS/WK : 05

NAME OF THE FACULTY : MrS.SUGANYA.S  
DATE OF COMMENCEMENT : 28.07.2016  
DATE OF CLOSING : 19.11.2016  
CLASS STRENGTH : 150(C,D&E)  
TOTAL HRS : 62

Session No	Chapter no (No of hrs planed for the chapter)	DATE	Topics planned for the session	Teaching Aids	Assignments/ Tests planned for the chapter	Topics covered As per plan
1	1/1		<b>Prerequisites:</b> Network basics, classifications and terminologies.	Board, chalk, duster		
2	2/1		<b>Prerequisites:</b> Basic laws, electrical elements, series parallel R, L, C combinations.	„		
3	3/1		<b>Prerequisites:</b> Redundancy, KVL&KCL Short and open circuit concepts Ideal and practical sources.	„		
4	4/1		<b>Prerequisites:</b> Series & Parallel connection (components & sources)	„		
5	5/1		<b>Prerequisites:</b> Redundancy, KVL&KCL	„	Assignment- I	
6	6/1		<b>Module-1:Basic concept</b> Ideal and practical sources.	Board, chalk, duster		
7	7/1		Source transformation, source shifting			
8	8/1		Voltage and current divider, Loop analysis	„		
9	9/1		Loop and node analysis With linearly dependent ac and dc sources	„		
10	10/1		Loop and node analysis With linearly independent sources	„		
11	11/1		Concepts of super node and super mesh.	„		
12	12/1		star-delta transformations	„		
13	13/1		PROBLEM Discussion	„		

14	1/2		<b>Module-2: Network Theorems</b> Superposition Theorem-Proof	Board, chalk, duster		
15	2/2		Problems on Superposition	„	Assignment - II	
16	3/2		Reciprocity Theorem -proof	„		
17	4/2		Reciprocity Theorem -Problems	„		
18	5/2		Millman’s theorem -Proof	„		
19	6/2		Millman’s theorem -Problems	„		
20	7/2		Thevinin’s Theorem -Proof	“		
21	8/2		Thevinin’s Theorem -Problems	„		
22	9/2		Norton’s theorem -Proof	„	Assignment –III	
23	10/2		Norton’s theorem -Problems	„		
24	11/2		Maximum Power transfer theorem	„		
25	12/2		Maximum Power transfer theorem Problems.	„		
26	13/2		Miller’s Theorem			
27	14/2		Miscellaneous PROBLEM Discussion	„		
28	1/4		<b>Module-4: Resonant Circuits</b> Series resonance	Board, chalk, duster		
29	2/4		Parallel Resonance	„	Assignment –IV	
30	3/4		Frequency response of series resonance	„		
31	4/4		Frequency response of Parallel resonance	„		
32	5/4		Q-factor, Bandwidth	„		
33	6/4		Problems on series resonance	„		
34	7/4		Problems on Parallel resonance	„		
35	8/4		Miscellaneous PROBLEM Discussion	„		
36	1/5		<b>Module-5: Two port Network Parameter</b> z-parameters	Board, chalk, duster		
37	2/5		y -parameters	„	Assignment -V	
38	3/5		h-parameters	„		
39	4/5		Transmission Parameters	„		
40	5/5		Relationship between parameters	„		
41	6/5		Series & cascade connection of 2 port n/w	„		
42	7/5		Problems	„		
	8/5		Miscellaneous problems/Revision	„		
43	1/3		<b>Module-3: Transient behavior and initial conditions:</b> Behavior of circuit elements under switching condition and their Representation	Board, chalk, duster		



44	<b>2/3</b>		Evaluation of initial and final conditions in RL for AC excitations	„		
45	<b>3/3</b>		Evaluation of initial and final conditions in RL circuits for DC excitations	„	Assignment -VI	
46	<b>4/3</b>		evaluation of initial and final conditions in RC circuits for AC excitations	„		
47	<b>5/3</b>		Evaluation of initial and final conditions in RC circuits for DC excitations	„		
48	<b>6/3</b>		evaluation of initial and final conditions in RLC circuits for AC excitations	„		
49	<b>7/3</b>		Evaluation of initial and final conditions in RLC circuits for DC excitations	„		
50	<b>8/3</b>		Problems	„		
51	<b>9/3</b>		<b>Laplace Transformation &amp; Applications :</b> Solution of networks	„		
52	<b>10/3</b>		Step responses	Board, chalk, duster	Assignment -VII	
53	<b>11/3</b>		Ramp responses	„		
54	<b>12/3</b>		Impulse responses	„		
55	<b>13/3</b>		waveform Synthesis	„	Assignment -VII	
56	<b>14/3</b>		Problems on waveform synthesis	„		
57	<b>15/3</b>		Laplace problems	„		
58	<b>16/3</b>		Miscellaneous problems/Revision	„		
59	<b>17/3</b>		Miscellaneous problems/Revision	„		
60	--		Revision	„		
61	--		Revision	„		
62	--		Revision	„		

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**Department of Electronics and Communication**

SEMESTER : III  
BRANCH : ECE  
SUBJECT : EI  
SUBJECT CODE : 15EC35  
NO OF HRS/WK : 5

NAME OF THE FACULTY:Nagratna Shanbhag/Preethi A/Padmaja D  
DATE OF COMMENCEMENT: 28.07.2016  
DATE OF CLOSING : 19.11.2016  
CLASS STRENGTH : 50/51/59/59/47  
TOTAL HRS : 58

Sessi on No	Chapter no (No of hrs planed for the chapter)	DATE	Topics planned for the session	Teaching Aids	Assignm ents/ Tests planned for the chapter	Topics covere d As per plan
1	1/1	28.07.16	Introduction to subject, Discussion on syllabus, Briefing on assignments and IAT.	Board, chalk, duster		
2	2/1	01.08.16	Measurement of error, Classification, causes and how to reduce error.	„		
3	3/1	02.08.16	Accuracy, Precision, Resolution and Significant figures.	„		
4	4/1	03.08.16	Numerical	„		
5	5/1	05.08.16	Measurement Error Combinations and numerical	„		
6	6/1	06.08.16	Numerical Contd.	„		
7	7/1	08.08.16	Basics of Statistical Analysis	„		
8	8/1	09.08.16	Basic D'Arsonval meter, DC ammeter, Multirange ammeter and Problems.	Board, chalk, duster		
9	9/1	10.08.16	Universal Shunt/Ayrton Shunt	„		

10	<b>10/1</b>	12.08.16	Numerical	„		
11	<b>11/1</b>	16.08.16	Requirements of shunt Extending of ammeter Range	„	Assignm ent- I	
12	<b>12/1</b>	17.08.16	RF ammeter (Thermocouple)	„		
13	<b>13/1</b>	18.08.16	Introduction, Basic Meter as a DC Voltmeter, DC Voltmeter	„		
14	<b>14/1</b>	19.08.16	Multirange Voltmeter, Extending Voltmeter Ranges, numerical	„		
15	<b>15/1</b>	22.08.16	Loading, Numerical			
16	<b>16/1</b>	23.08.16	Transistor Voltmeter, AC Voltmeter using Rectifiers			
17	<b>17/1</b>	24.08.16	Numerical			
18	<b>18/1</b>	25.08.16	Differential Voltmeter, True RMS Voltmeter, Considerations in Choosing an Analog Voltmeter	„		
19	<b>19/1</b>	26.08.16	Analog Multimeter	„		
20	<b>1/2</b>	29.08.16	Introduction, RAMP technique, Dual Slope Integrating Type DVM, Integrating Type DVM	„		
21	<b>2/2</b>	30.08.16	Most Commonly used principles of ADC, Successive Approximations	„	Assignm ent -II	
22	<b>3/2</b>	31.08.16	Numerical	„		
23	<b>4/2</b>	01.09.16	Continuous Balance DVM, - Digit, Resolution and Sensitivity of Digital Meters, General Specifications of DVM			
24	<b>5/2</b>	02.09.16	Microprocessor based Ramp type DVM	Board, chalk, duster		
25	<b>6/2</b>	10.09.16	Digital Multimeters	„		

26	<b>7/2</b>	13.09.16	Digital Frequency Meter	„		
27	<b>8/2</b>	14.09.16	Digital Measurement of Time	„		
28	<b>9/2</b>	15.09.16	Universal Counter, Digital Tachometer	„		
29	<b>10/2</b>	16.09.16	Digital pH Meter, Digital Phase Meter	„		
30	<b>11/2</b>	19.09.16	Digital Capacitance Meter, Microprocessor based Instruments	„		
31	<b>1/3</b>	20.09.16	Oscilloscopes: Introduction, Basic principles, CRT features, Block diagram of Oscilloscope	„		
32	<b>2/3</b>	21.09.16	Simple CRO, Vertical Amplifier	„		
33	<b>3/3</b>	22.09.16	Horizontal Deflecting System, Sweep or Time Base Generator	Board, chalk, duster	Assignm ent -III	
34	<b>4/3</b>	23.09.16	Storage Oscilloscope, Digital Readout Oscilloscope	„		
35	<b>5/3</b>	26.09.16	Measurement of Frequency by Lissajous Method	„		
36	<b>6/3</b>	27.09.16	Digital Storage Oscilloscope	„		
37	<b>7/3</b>	28.09.16	Signal Generators: Introduction, Fixed and Variable AF Oscillator, Standard Signal Generator	„		
38	<b>8/3</b>	29.09.16	Laboratory Type Signal Generator	„		
39	<b>9/3</b>	03.10.16	AF sine and Square Wave Generator, Function Generator	„		
40	<b>10/3</b>	05.10.16	Square and Pulse Generator, Sweep Generator	„		
41	<b>1/4</b>	06.10.16	Bridges: Introduction, Wheatstone's bridge, Numerical	„		
42	<b>2/4</b>	07.10.16	Kelvin's Bridge	„		

43	3/4	08.10.16	Numerical	„	Assignment –IV	
44	4/4	13.10.16	AC bridges, Capacitance Comparison Bridge, Inductance Comparison Bridge	„		
45	5/4	17.10.16	Numerical	„		
46	6/4	18.10.16	Maxwell’s bridge, Wein’s bridge	„		
47	7/4	19.10.16	Numerical	„		
48	8/4	20.10.16	Wagner’s earth connection	„		
49	9/4	21.10.16	Measuring Instruments: Output Power Meters, Field Strength Meter	Board, chalk, duster		
50	10/4	22.10.16	Stroboscope, Phase Meter	„		
51	11/4	27.10.16	Vector Impedance Meter, Q Meter	„	Assignment -V	
52	12/4	28.10.16	Numerical	„		
53	13/4	02.11.16	Megger, Analog pH Meter.	„		
54	1/5	03.11.16	Transducers: Introduction, Electrical transducers, Selecting a transducer, Resistive transducer, Resistive position transducer	„		
55	2/5	04.11.16	Strain gauges, Resistance thermometer, Thermistor, Inductive transducer	„		
56	3/5	05.11.16	Differential output transducers, LVDT, Piezoelectric transducer, Photoelectric transducer	„		
57	4/5	07.11.16	Photovoltaic transducer, Semiconductor photo diode and transistor	„		
58	5/5	08.11.16	Temperature transducers-RTD	„		

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Signature of Principal

**Department of Electronics and Communication**

SEMESTER :III	NAME OF THE FACULTY : Sutapa Sarkar/ Suganya.J
BRANCH : ECE	DATE OF COMMENCEMENT : 28.07.2016
SUBJECT : Engineering Electromagnetics	DATE OF CLOSING : 9.11.2016
SUBJECT CODE : 15EC36	CLASS STRENGTH : 60
NO OF LECTURES/WK : 5	TOTAL HRS : 60

Sessi on No	Chapter no (No of hrs planned for the chapter)	DATE	Topics planned for the session	Teaching Aids	Assignm ents/ Tests planned for the chapter	Topics cover ed As per plan
1	1/3	29.07.2016	Prerequisites: Introduction to Vector Calculus	Board, chalk, duster		
2	2/3	01.08.2016	3D co-ordinate system(Cartesian)	„		
3	3/3	02.08.2016	3D co-ordinate system(Cylindrical), 3D co-ordinate system(spherical)	„		
4	1/7	3.08.2016	Coulomb’s Law and problems			
5	2/7	05.08.2016	Electric field intensity, problems			
6	3/7	06.08.2016	Problems on electric field intensity, field due to continuous volume charge distribution		Assignm ent- I	
7	4/7	08.08.2016	Field of a line charge(infinite)			
8	5/7	09.08.2016	field of a line charge(finite)			
9	6/7	10.08.2016	Field due to ring of charge and problems	„		

10	7/7	12.08.2016	Surface charge and problems(disc charge)	„		
11	1/5	16.08.2016	Electric flux density, Gauss' law	„		
12	2/5	17.08.2016	Gauss's divergence theorem.	„		
13	3/5	18.08.2016	Maxwell's First equation of electrostatics, Verification of Gauss's divergence theorem in Cartesian co-ordinates	„		
14	4/5	19.08.2016	Verification of Gauss's divergence theorem in Cylindrical and spherical co-ordinates	„		
15	5/5	22.08.2016	Problem	„		
16	1/8	23.08.2016	Work(Energy expended in moving a point charge) and Potential, The line integral	„	Assignment -II	
17	2/8	24.08.2016	Problems on work and potential			
18	3/8	25.08.2016	Definition of potential difference and Potential, The potential field of a point charge and system of charges,			
19	4/8	26.08.2016	Potential gradient and related problems			
20	5/8	29.08.2016	Energy density equation and problems	„		
21	6/8	30.08.2016	Current and current density, Continuity of current,	„		
22	7/8	31.08.2016	relaxation time,	„		
23	8/8	01.09.2016	revision	„		
			<b><u>Internal 1</u></b>			
24	1/3	13.09.2016	Poisson's and Laplace's equation Uniqueness theorem,	„		
25	2/3	14.09.2016	Capacitance of coaxial cylinder sphere using Laplace's equation.	Board, chalk, duster		

26	3/3	15.09.2016	Example of angular variation in spherical co-ordinates. Problem practice.	„		
27	1/11	16.09.2016	Magnetic field intensity, Biot-Savart's law.	„	Assignment –III	
28	2/11	19.09.2016	Magnetic field intensity for infinite line conductor.	„		
29	3/11	20.09.2016	Magnetic field intensity for finite line conductor.	„		
30	4/11	21.09.2016	Problem on Magnetic field intensity for finite conductor – ring conductor, Ampere's Circuital Law	„		
31	5/11	22.09.2016	Ampere's Circuital Law for co-axial cable	„		
32	6/11	23.09.2016	Infinite sheet of charge, solenoid	„		
33	7/11	26.09.2016	Curl problems, Point form of Ampere's Circuital Law.	Board, chalk, duster		
34	8/11	27.09.2016	Stoke's theorem and problems	„		
35	9/11	28.09.2016	Stokes's theorem verification	„		
36	10/11	29.09.2016	Stokes's theorem verification	„		
37	11/11	03.10.2016	Magnetic flux and flux density, scalar and Vector magnetic potentials.	„		
38	1/6	05.10.2016	Force on a moving charge and related problems	„		
39	2/6	06.10.2016	differential current element, Force between differential current elements, problem	„	Assignment -IV	
40	3/6	07.10.2016	Torque problem	„		
41	4/6	08.10.2016	Magnetic materials, Magnetization and permeability	„		
42	5/6	13.10.2016	Magnetic boundary conditions	„		



43	6/6	17.10.2016	Magnetic circuit, Potential energy and forces on magnetic materials,	„		
44	1/5	18.10.2016	Faraday's law, point form of Faraday's law.	„		
45	2/5	19.10.2016	Displacement current, Modified form of Ampere's law.	„		
46	3/5	20.10.2016	Maxwell's equation in point and Integral form,	„		
47	4/5	21.10.2016	retarded potentials.	„		
48	5/5	22.10.2016	Problem	„		
			<b><u>Internal 2</u></b>			
49	1/5	28.10.2016	Wave propagation in free space	Board, chalk, duster	Assignment -V	
50	2/5	02.11.2016	Wave propagation in dielectrics	„		
	3/5	03.11.2016	Calculation of intrinsic impedance	„		
51	4/5	04.11.2016	propagation in good conductors – (skin effect).	„		
52	5/5	07.11.2016	Poynting's theorem and wave power,	„		
53		08.11.2016	Problems	„		
54		09.11.2016	Revisions	„		
55			Revisions	„		

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