

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Class : **7TH Semester 'A' Section**
Class Teacher : Prof . EISHA

Room No. : **LH 301**
Session : **JUL 2016 – NOV 2016**

Time Day	08:00 - 09:00 AM	09:00 - 09:50 AM	9:50 - 10:40 AM	SHORT BREAK (10:40- 11:10 AM)	11:10 - 12:00 PM	12:00 - 12:50 PM	LUNCH BREAK (12:50 – 13:30 PM)	13:30 - 14:20 PM	14:20 - 15:10 PM	15:10 - 16:00 PM
D1	A1 PE LAB / VLSI LAB A2				CCN	OFC		PE	IP	
D2	A3 PE LAB / VLSI LAB A1				DSPA	OFC		IP	CCN	ESD
D3	IP	PE	DSPA		ESD	CCN		A2 PE LAB / VLSI LAB A3		
D4	ESD	CCN	DSPA		PE	PE		COUNSELING		
D5	OFC	LI-PE	ESD		DSPA	PE		OFC	D5	OFC
D6	IP	LI-VLSI	DSPA		CCN	OFC		ESD	D6	IP

Sub Code	Subject	Faculty	A1	A2	A3
10EC71	COMPUTER COMMUNICATION NETWORK (CCN)	Prof. EISHA	LAB-01 : POWER ELECTRONICS (PE LAB)		
10EC72	OPTICAL FIBER COMMUNICATION (OFC)	Prof. SUDHIR	Prof Harsha , Prof Naveenkumar	Prof Tania , Prof Naveenkumar	Prof Tania , Prof Naveenkumar
10EC73	POWER ELECTRONICS (PE)	Prof. Tania	LAB-02 : VLSI LAB (VLSI LAB)		
10EC74	EMEDDED SYSTEM DESIGN(ESD)	Prof. PUSHPA	Prof Mahesh, Prof Madhusudan	Prof Mahesh, Prof Madhusudan	Prof Mahesh, Prof Madhusudan
10EC751	ELECTIVE-I (DSP ARCHITECTURE)	Prof. PAPPA M	Mentors		
10EC763	ELECTIVE –II (IMAGE PROCESSING (IP))	Prof. ARCHANA			
L I CLASS	LAB Instruction Class	LI-PE LI-VLSI	Prof Tania Prof Mahesh	CR's: Akash C Mathew, Aishwarya Singh	

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Class : **7TH Semester 'B' Section**

Room No. : **LH 302**

Class Teacher : Prof . PUSHPA

Session : JUL 2016 – NOV 2016

Time Day	08:00 - 09:00 AM	09:00 - 09:50 AM	9:50 - 10:40 AM	SHORT BREAK (10:40- 11:10 AM)	11:10 - 12:00 PM	12:00 - 12:50 PM	LUNCH BREAK (12:50 – 13:30 PM)	13:30 - 14:20 PM	14:20 - 15:10 PM	15:10 - 16:00 PM		
D1	PE	OFC	CCN		SHORT BREAK (10:40- 11:10 AM)	IP		DSPA	LUNCH BREAK (12:50 – 13:30 PM)	B1 PE LAB / VLSI LAB B2		
D2	CCN	PE	DSPA			ESD		IP		OFC	D2	CCN
D3	B2 PE LAB / VLSI LAB B3					CCN		DSPA		ESD	IP	OFC
D4	CCN	PE	IP			ESD		OFC		COUNSELING		
D5	PE	LI-VLSI	OFC			CCN		IP		DSPA	D5	PE
D6	DSPA	LI-PE	ESD			PE		ESD		B3 PE LAB / VLSI LAB B1		

Sub Code	Subject	Faculty	B1	B2	B3
10EC71	COMPUTER COMMUNICATION NETWORK (CCN)	Prof. EISHA	LAB-01 : POWER ELECTRONICS (PE LAB)		
10EC72	OPTICAL FIBER COMMUNICATION (OFC)	Prof. SUDHIR	Prof Harsha BK	Prof Naveenkumar	Prof Naveenkumar, Prof Tania
10EC73	POWER ELECTRONICS (PE)	Prof Tania	LAB-02 : VLSI LAB (VLSI LAB)		
10EC74	EMEDDED SYSTEM DESIGN(ESD)	Prof. PUSHPA	Prof Madhusudan, Prof Amit Jain	Prof Mahesh, Prof Vinay	Prof Vinay, Prof sunil
10EC751	ELECTIVE-I (DSP ARCHITECTURE)	Prof. PAPPA M	Mentors		
10EC763	ELECTIVE –II (IMAGE PROCESSING (IP))	Prof. ARCHANA			
L I CLASS	LAB Instruction Class	LI-PE LI-VLSI	Prof Harsha BK Prof Vinay	CR's: Prasanna kumar H, Poornima	

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Class : **7TH Semester 'C' Section**
Class Teacher : Prof . AMMU

Room No. : **LH 303**
Session : JUL 2016 – NOV 2016

Time Day	08:00 - 09:00 AM	09:00 - 09:50 AM	9:50 - 10:40 AM	SHORT BREAK (10:40 - 11:10 AM)	11:10 - 12:00 PM	12:00 - 12:50 PM	LUNCH BREAK (12:50 - 13:30 PM)	13:30 - 14:20 PM	14:20 - 15:10 PM	15:10 - 16:00 PM
D1	CCN	DSPA	PE		IP	OFC		PE	ESD	
D2	DSPA	DSPA	OFC		CCN	IP		C2 PE LAB / VLSI LAB C3		
D3	ESD	OFC	ESD		PE	CCN		LI-PE	IP	DSPA
D4	IP	CCN	LI-VLSI		OFC	PE		COUNSELING		
D5	C1PE LAB / VLSI LAB C2				ESD	IP		OFC		
D6	C3PE LAB / VLSI LAB C1				PE	ESD		DSPA	CCN	

Sub Code	Subject	Faculty	C1	C2	C3
10EC71	COMPUTER COMMUNICATION NETWORK (CCN)	Prof. JYOTI R	LAB-01 : POWER ELECTRONICS (PE LAB)		
10EC72	OPTICAL FIBER COMMUNICATION (OFC)	Prof. SHAILEE	Prof Harsha BK	Prof Naveenkumar	Prof Jagdeesh Prof Tania
10EC73	POWER ELECTRONICS (PE)	Prof. JAGADISH	LAB-02 : VLSI LAB (VLSI LAB)		
10EC74	EMEDDED SYSTEM DESIGN(ESD)	Prof. BHUMIKA	Prof Amit Jain, Prof sunil	Prof Amit Jain, Prof Sunil K H	Prof Vinay, Prof Sunil K H
10EC751	ELECTIVE-I (DSP ARCHITECTURE)		Mentors		
10EC763	ELECTIVE –II (IMAGE PROCESSING (IP)	Prof. AMMU			
L I CLASS	LAB Instruction Class	Prof Jagdeesh ANKIT	CR's: Sai Kishore, Varshitha		

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Class : **7TH Semester 'D' Section**
Class Teacher : Prof . JYOTI. R

Room No. : **LH 306**
Session : JUL 2016 – NOV 2016

Time Day	08:00 - 09:00 AM	09:00 - 09:50 AM	9:50 - 10:40 AM	SHORT BREAK (10:40- 11:10 AM)	11:10 - 12:00 PM	12:00 - 12:50 PM	LUNCH BREAK (12:50 – 13:30 PM)	13:30 - 14:20 PM	14:20 - 15:10 PM	15:10 - 16:00 PM			
D1	DSPA	OFC	CCN		SHORT BREAK (10:40- 11:10 AM)	D1 PE LAB / VLSI LAB D2		LUNCH BREAK (12:50 – 13:30 PM)					
D2	CCN	IP	PE			ESD			OFC	D2	CCN	IP	
D3	DSPA	DSPA	OFC			IP			ESD	D3	DSPA	DSPA	
D4	D3 PE LAB / VLSI LAB D1					IP			ESD	COUNSELING			
D5	PE	IP	OFC			LI-VLSI			CCN	D5			
D6	IP	OFC	ESD			DSPA			LI-PE	D6	IP	OFC	

Sub Code	Subject	Faculty	D1	D2	D3
10EC71	COMPUTER COMMUNICATION NETWORK (CCN)	Prof. JYOTI R	LAB-01 : POWER ELECTRONICS (PE LAB)		
10EC72	OPTICAL FIBER COMMUNICATION (OFC)	Prof. SHAILEE	Prof Jagdeesh, Prof Tania	Prof Jagdeesh, Prof Harsha BK	Prof Jagdeesh
10EC73	POWER ELECTRONICS (PE)	Prof. JAGADISH	LAB-02 : VLSI LAB (VLSI LAB)		
10EC74	EMEDDED SYSTEM DESIGN(ESD)	Prof. BHUMIKA	Prof Sunil KH	Prof Sunil KH	Prof Vinay, Prof Amit Jain
10EC751	ELECTIVE-I (DSP ARCHITECTURE)		Mentors		
10EC763	ELECTIVE –II (IMAGE PROCESSING (IP)	Prof. AMMU			
L I CLASS	LAB Instruction Class	Prof Naveenkumar Prof SUNIL	CR's: Ambika, Noni Dalal		

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