

Class : 6th Semester 'A' Section

Room No. : LH-304

Class Teacher : PROF SUMA S

Session : JAN – JUNE 2017

Time Day	08:00 – 09:00	09:00 – 09:50	9:50 – 10:40		11:10 – 12:00	12:00 – 12:50		13:40 – 14:30	14:30 – 15:20	15:20 – 16:10	
D1	DSDV/ SC/ C++	DSDV/ SC/ C++	MP	SHORT BREAK (10:40- 11:10 AM)	A3 MP (SUT,SUJ)/ A2 ADC(MKJ,SAT)		LUNCH BREAK (12:50 – 13:30 PM)		MEC	AWP	
D2	DC	DC	DSDV/ SC/ C++		OS	MP		DSDV/ SC/ C++	Prepare Program		
D3	MEC	MEC	LI(ADC)		A2 MP(JYT, SUJ) /A1 ADC(RAV, SAT)				OS	OS	
D4	AWP	MEC	MEC		DC	DC		COUNSELLING			
D5	MP	AWP	MP		LI(MP)	OS		OS	MEC	AWP	
D6	DC	DC	DSDV/ SC/ C++		AWP	MP		A1 MP (SRI, CHE) /A3 ADC(RAV, SAT)			

Sub Code	Subject	Faculty	A1	A2	A3
10EC61	Digital Communication	Prof. Muralishankar	LAB-01 : Microprocessors		
10EC62	Microprocessors	Prof. Suma S	Prof. Chetan Prof. Sridevi	Prof. Jyoti Prof. Sujatha	Prof. Sutapa Prof. Sujatha
10EC63	Microelectronics Circuits	Prof. Abdul Nasir	LAB-02 : Analog & Digital Communication		
10EC64	Antennas and Propagation	Prof. Kalaga Madhav	Prof. Raveesh Prof. Satish	Prof. Mahesh Jha Prof. Satish	Prof. Raveesh Prof. Satish
10EC65	Operating Systems	Prof. Priyanka R	Mentors		
10EC662	Satellite Communication	Prof. Kalaga Madhav			
10EC665	Data structure using C++	Prof. P N Singh			
10EC666	Digital system design using Verilog	Prof. Chetan			
L I CLASS	LAB Instruction Class (LI)	MP ADC	Prof. Sutapa Prof. Satish	CR's:	

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HOD (ECE)

PRINCIPAL

Class : 6th Semester 'B' Section

Room No. : LH -305

Class Teacher : PROF SUNIL KUMAR

Session : JAN - JUNE 2017

Time Day	08:00 -09:00 AM	09:00 - 09:50 AM	9:50 - 10:40 AM	S H O R T B R E A K (1 0 : 4 0 - 1 1 : 1 0 A M)	11:10 - 12:00 PM	12:00 - 12:50 PM	L U N C H B R E A K (1 2 : 5 0 - 1 3 : 3 0 P M)	13:40 - 14:30 PM	14:30 -15:20 PM	15:20 - 16:10 PM	
D1	DSDV/ SC/ C++	DSDV/ SC/ C++	OS		OS	MP		B3 MP(SRI, CHE) /B2 ADC(MON, JAG,SWR)			
D2	MEC	LI(ADC)	DSDV/ SC/ C++		DC	DC		DSDV/ SC/ C++	Prepare Program		
D3	B1 MP(SUJ,SJP) /B3 ADC(MON,RAV)				MP	AWP		MP	MEC	MEC	
D4	DC	DC	OS		MP	AWP		COUNSELLING			
D5	OS	MEC	LI(MP)		B2 MP(JYT, CHE)/ B1 ADC(RAV, SAT)			MEC	AWP		
D6	MP	MEC	DSDV/ SC/ C++	DC	DC	AWP	OS	AWP			

Sub Code	Subject	Faculty	B1	B2	B3
10EC61	Digital Communication	Prof. R Muralishankar	LAB-01 : Microprocessors		
10EC62	Microprocessors	Prof. Sunil kumar	Prof Sujatha Prof Sutapa	Prof. Jyoti Prof. Chetan	Prof. Sridevi Prof. Chetan
10EC63	Microelectronics Circuits	Prof. Abdul Nasir	LAB-02 : Analog & Digital Communication		
10EC64	Antennas and Propagation	Prof. Kalaga Madhav	Prof. Raveesh H Prof. Satish	Prof. Jagruti Prof. Monika Prof. Swathi R	Prof. Jagruti Prof. Monika

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

10EC65	Operating Systems	Prof. Priyanka R	Mentors		
10EC662	Satellite Communication	Prof. Kalaga Madhav			
10EC665	Data structure using C++	Prof P N Singh			
10EC666	Digital system design using Verilog	Prof. Chetan			
L I CLASS	LAB Instruction Class (LI)	MP ADC	Prof. Suganya J P Prof. Monika	CR's:	

Class : 6th Semester 'C' Section

Room No. : CV-403

Class Teacher : PROF. SUTAPA

Session : JAN – JUNE 2017

Time Day	08:00 -09:00 AM	09:00 - 09:50 AM	9:50 - 10:40 AM	S H O R T B R E A K (1 0: 4 0- 1 1: 1 0 A M)	11:10 - 12:00 PM	12:00 - 12:50 PM	L U N C H B R E A K (12 :50 - 13: 30 P M)	13:40 - 14:30 PM	14:30 -15:20 PM	15:20 - 16:10 PM
D1	DSDV/ SC/ C++	DSDV/ SC/ C++	OS		DC	DC		MEC	AWP	AWP
D2	OS	AWP	DSDV/ SC/ C++		C1 MP(SUT,SJP)/ C2 ADC(RAH, MKJ)	DSDV/ SC/ C++		Prepare Program		
D3	MP	MEC	OS		DC	DC		C3 MP(SRI,CHE) /C1 ADC(JAG,RAH,SWR)		
D4	AWP	MP	OS		MEC	MEC		COUNSELLING		
D5	C2 MP(SUT,SRI) /C3 ADC(MON,RAH)				LI(ADC)	MP		MEC	MP	MEC
D6	DC	DC	DSDV/ SC/ C++		LI(MP)	OS		AWP	MP	

Sub Code	Subject	Faculty	C1	C2	C3
10EC61	Digital Communication	Prof. Rahul N	LAB-01 : Microprocessors		

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

10EC62	Microprocessors	Prof. Suma S	Prof. Sutapa Prof. Suganya J P	Prof. Sujatha Prof Sridevi	Prof. Sridevi Prof. Chetan
10EC63	Microelectronics Circuits	Prof. Nagaratna	LAB-02 : Analog & Digital Communication		
10EC64	Antennas and Propagation	Prof. Sutapa	Prof. Jagruti Prof. Rahul N Prof Swathi	Prof. Rahul N Prof. Mahesh	Prof. Rahul N Prof. Monika
10EC65	Operating Systems	Prof. Madhuri D H	Mentors		
10EC662	Satellite Communication	Prof. Kalaga Madhav			
10EC665	Data structure using C++	Prof. Poonam			
10EC666	Digital system design using Verilog	Prof. Chetan			
L I CLASS	LAB Instruction Class (LI)	MP ADC	Prof. Chetan Prof. Rahul N	CR's:	

Class : 6th Semester 'D' Section
Class Teacher : PROF. NAGARATNA

Room No. : CV-404
Session : JAN – JUNE 2017

Time Day	08:00 -09:00 AM	09:00 - 09:50 AM	9:50 - 10:40 AM	SHORT BREAK (10:40 - 11:10 AM)	11:10 - 12:00 PM	12:00 - 12:50 PM	LUNCH BREAK (12:50 - 13:30 PM)	13:40 - 14:30 PM	14:30 -15:20 PM	15:20 - 16:10 PM	
D1	DSDV/ SC/ C++	DSDV/ SC/ C++	AWP		OS	DC		DC	MEC		
D2	LI(MP)	MEC	DSDV/ SC/ C++		DC	OS		DSDV/ SC/ C++	Prepare Program		
D3	AWP	AWP	MP		OS	MP		MEC	MEC	AWP	
D4	MEC	MP	AWP		D1 MP(SJP,SUJ)/ D2 ADC(MON,JAG)			COUNSELLING			
D5	DC	DC	MEC		OS	LI(ADC)		D2 MP (SJP,JYT)/D3 ADC(MKJ,JAG)			
D6	DC	OS	DSDV/ SC/ C++		D3 MP (JYT,SUT)/D1 ADC (MKJ, RAH)				MP	MP	

Sub Code	Subject	Faculty	D1	D2	D3
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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

10EC61	Digital Communication	Prof. Raveesh H	LAB-01 : Microprocessors		
10EC62	Microprocessors	Prof. Sunil kumar	Prof. Sujata Prof. Suganya J	Prof. Suganya J Prof. Jyoti	Prof. Jyoti Prof. Sutapa
10EC63	Microelectronics Circuits	Prof. Nagaratna	LAB-02 : Analog & Digital Communication		
10EC64	Antennas and Propagation	Prof. Sutapa S	Prof. Rahul N Prof. Mahesh Jha	Prof. Monika Prof. Jagrati	Prof. Jagrati Prof. Mahesh Jha
10EC65	Operating Systems	Prof. Madhuri D H	Mentors		
10EC662	Satellite Communication	Prof. Kalaga Madhav			
10EC665	Data structure using C++	Prof. Poonam			
10EC666	Digital system design using Verilog	Prof. Chetan			
L I CLASS	LAB Instruction Class (LI)	MP ADC	Prof. Sujatha, Prof. Sridevi, Prof. Jyoti Prof. Mahesh Jha	CR's:	

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